

REVISION HISTORY

5

4

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1

Schematics Index:

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- P04: CPU
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| Revision | Description | Date | Drawn | Checked |
|----------|-------------|----------|--------|---------|
| V1_0 | Initial | 20180204 | Victor | |
| V1_1 | | 20180610 | Victor | |

D

D

C


C

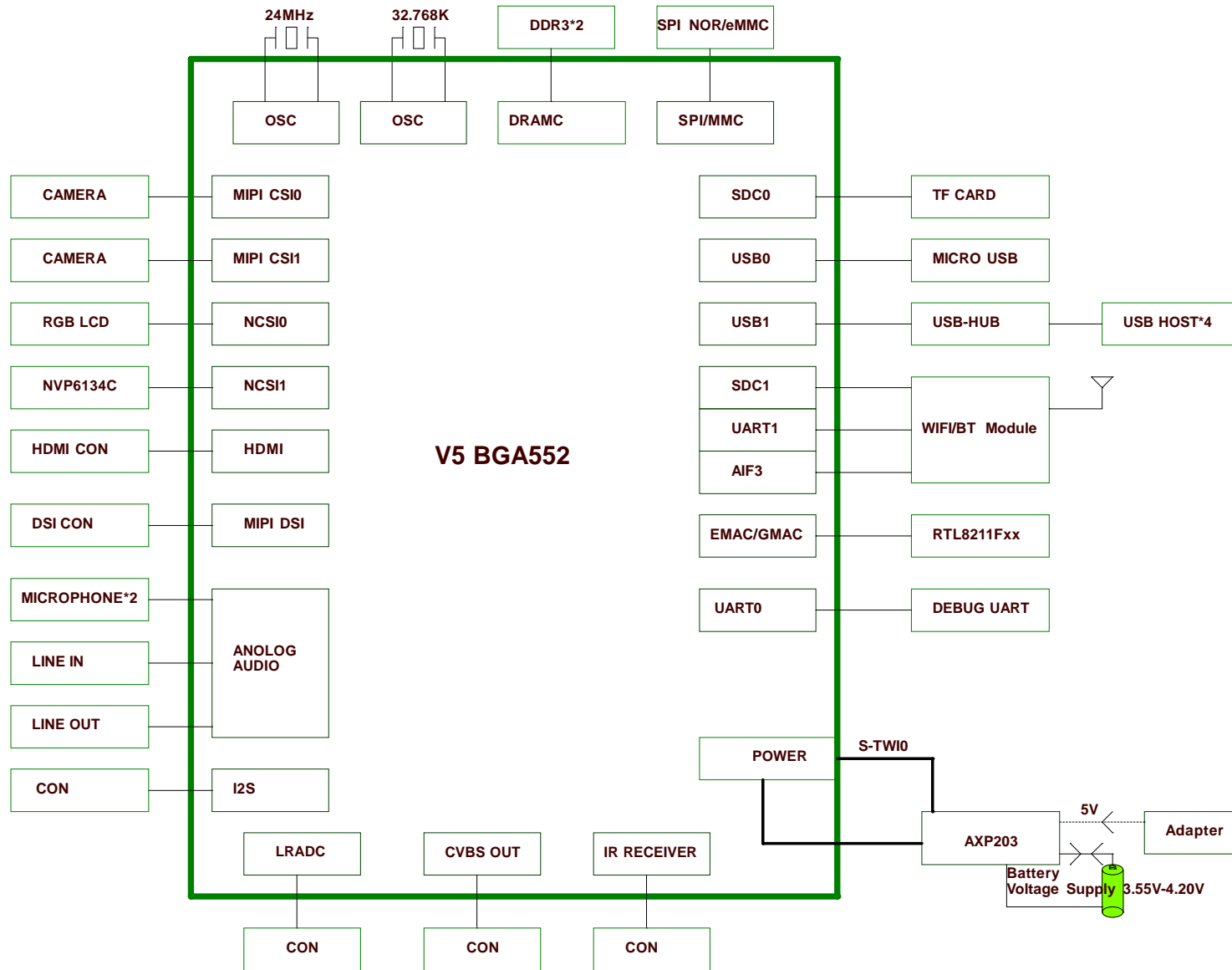
B

B

A

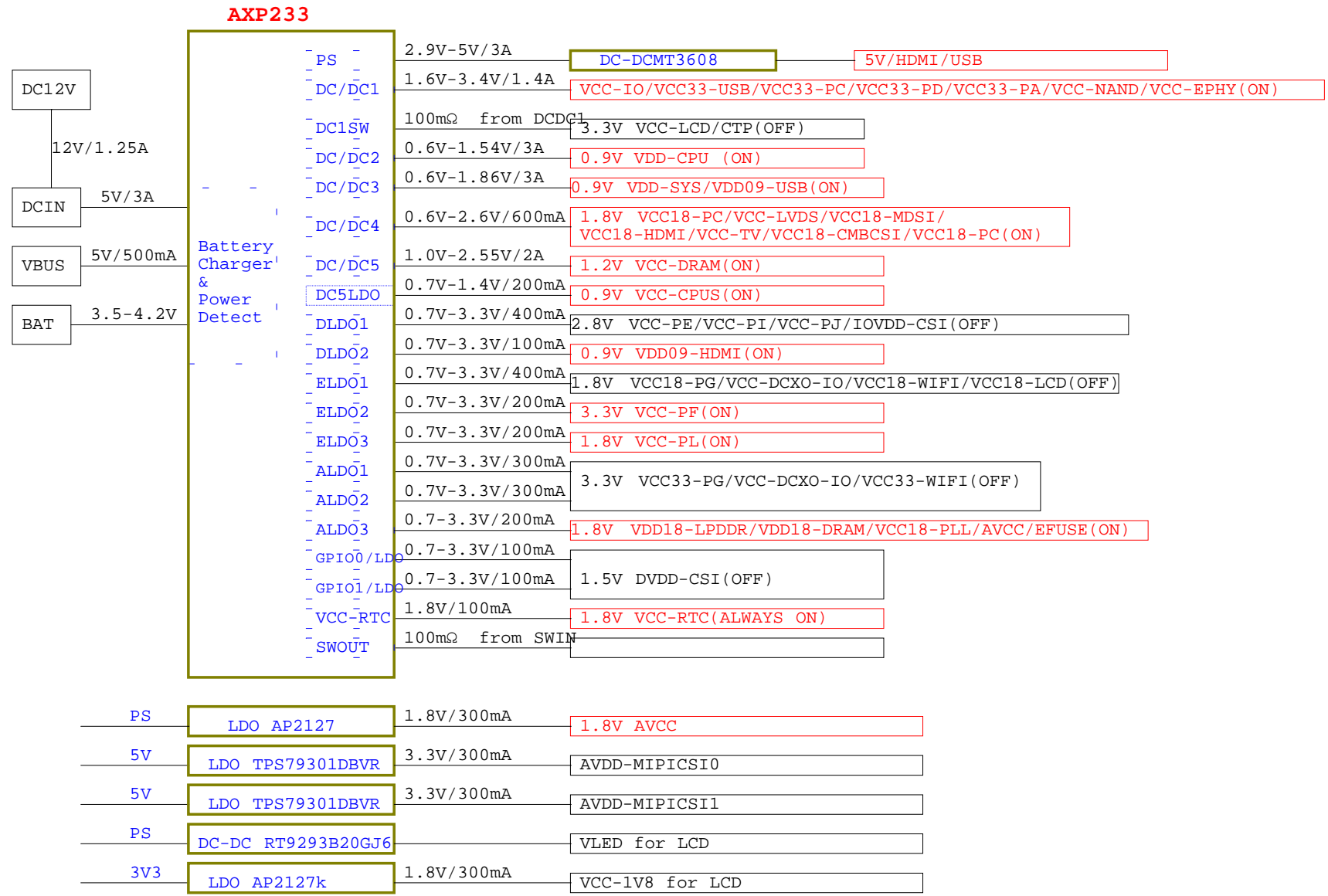
A

| | | | | | |
|---|------------------|--|--------------------------|--|--|
|  | | | Linden Tech. Ltd. | | |
| | | | Design Name | | |
| Size | Page Name | | Rev | | |
| A3 | REVISION HISTORY | | | | |
| Date: Friday, August 24, 2018 | | | Sheet 1 of 14 | | |

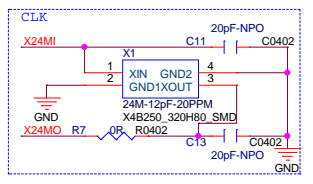
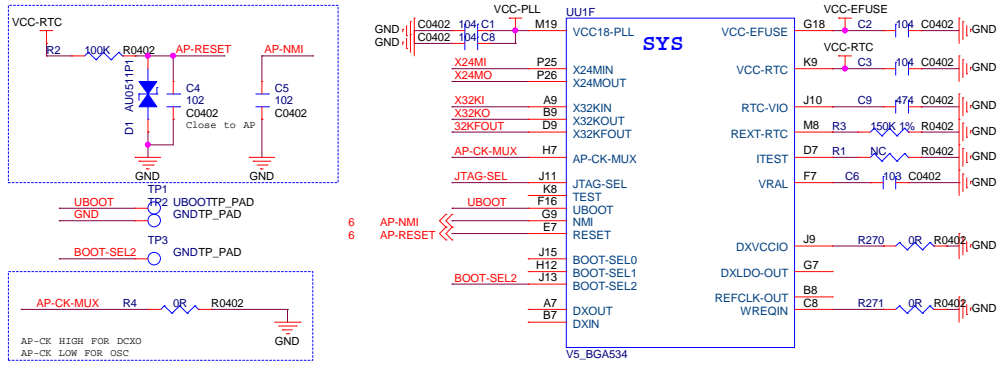


POWER TREE

DEFAULT POWER ON
 DEFAULT POWER OFF



SEPERATED WITH GND TRACE AND FAR AWAY FROM THE BOUNDARY

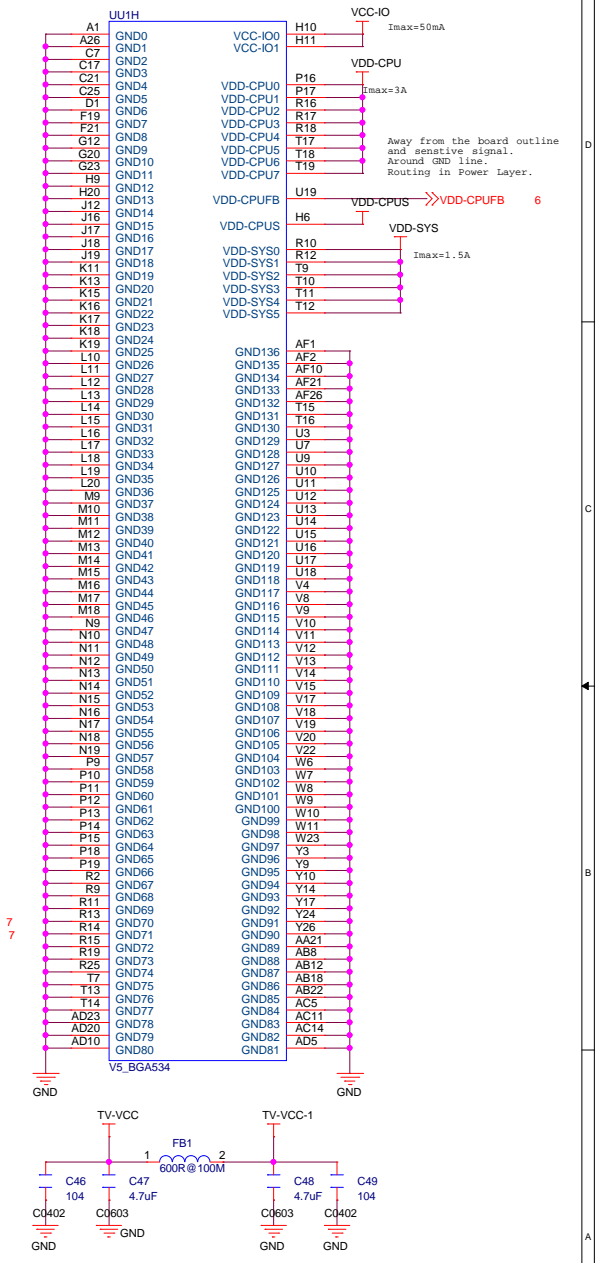
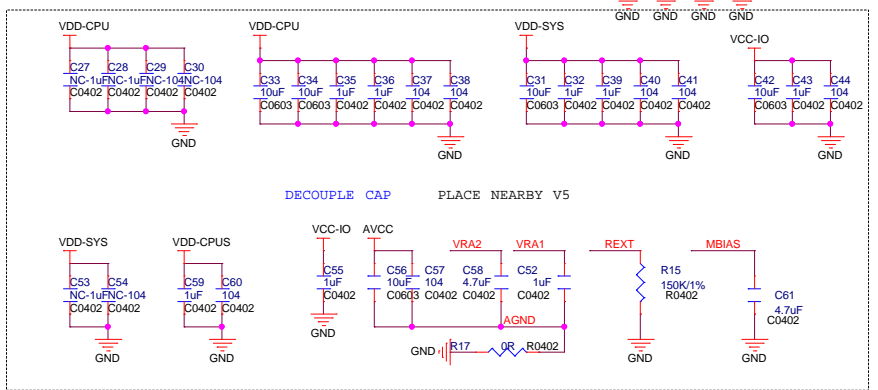
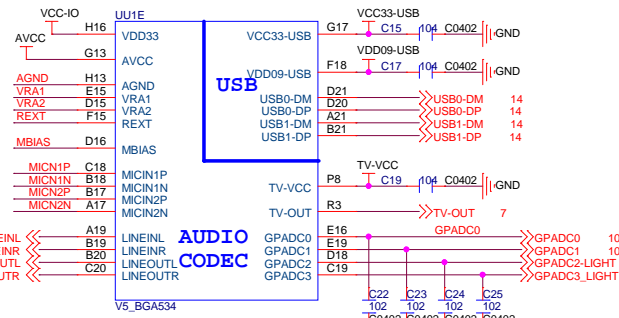
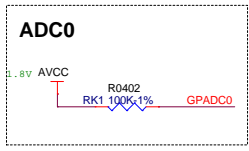
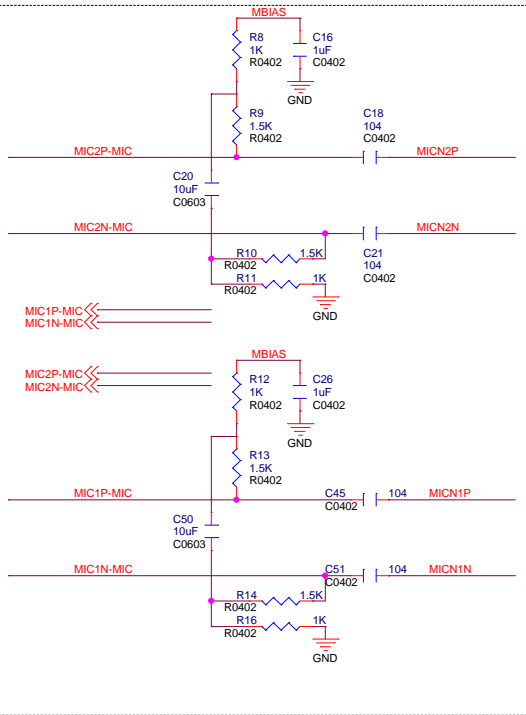


BOOT-SEL2: 0 UART UPGRADE; 1 USB UPGRADE
 BOOT-SEL[1:0]
 11 SMHC0-->SPI NOR-->SMHC2-->SPI NAND-->NAND FLASH

JTAG-SEL: R5 NC/R R0402 || GND

JTAG-SEL:
 1: Choose JTAG function from PB or PF by SW setting
 0: Force JTAG Function outputted from PB
 Internal pull high

13 X32KFOUT <<32KFOUT



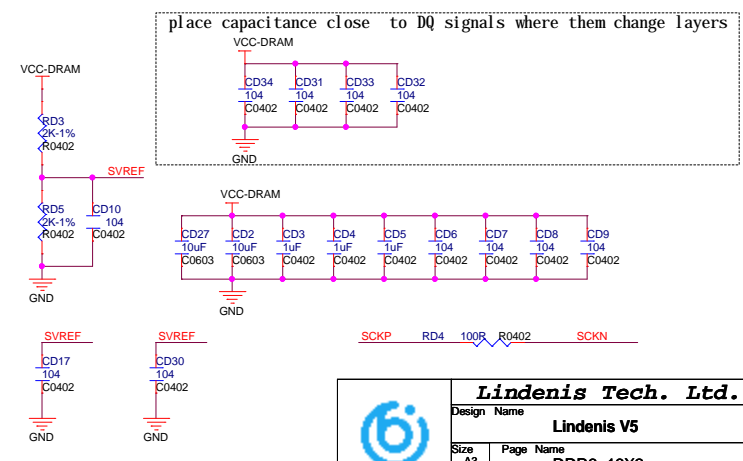
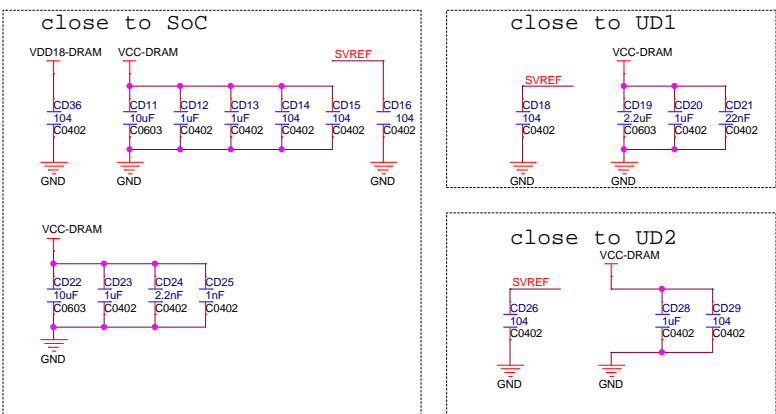
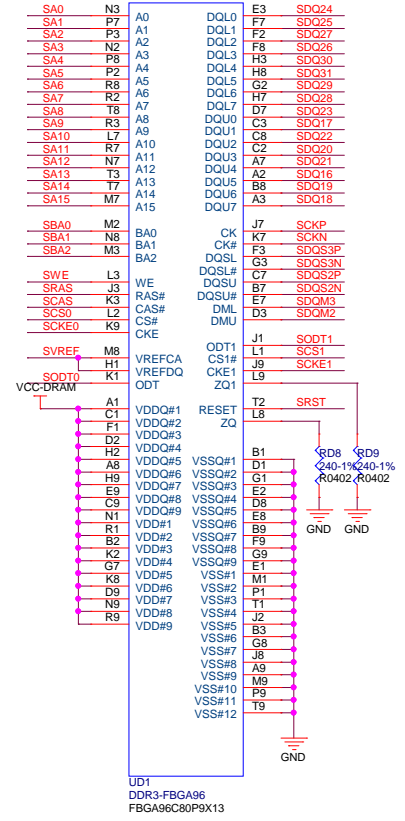
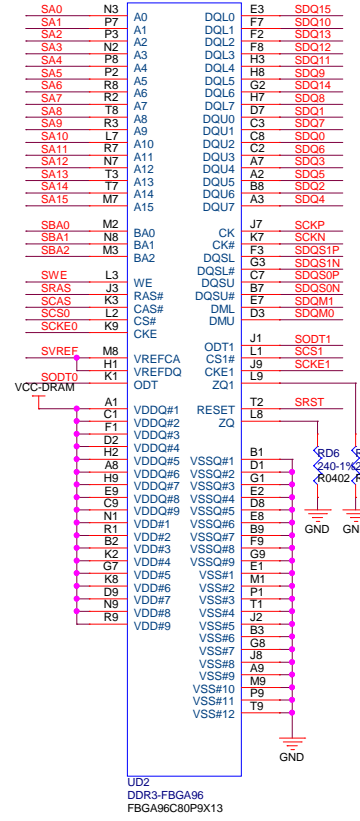
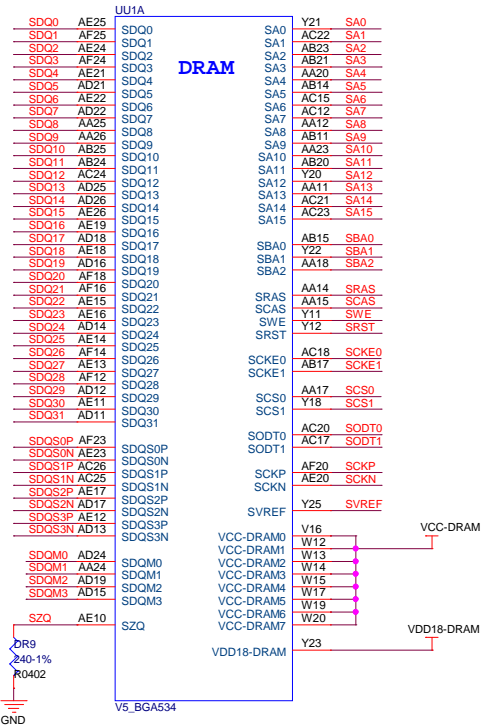
Linden Tech. Ltd.

Design Name: Linden V5

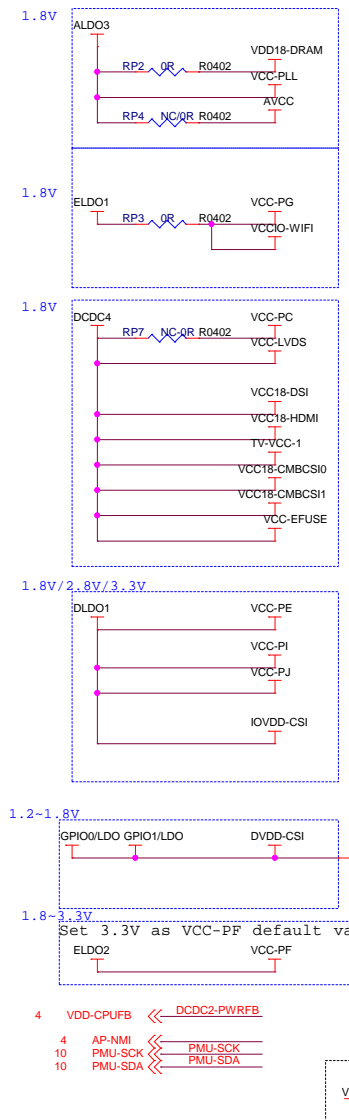
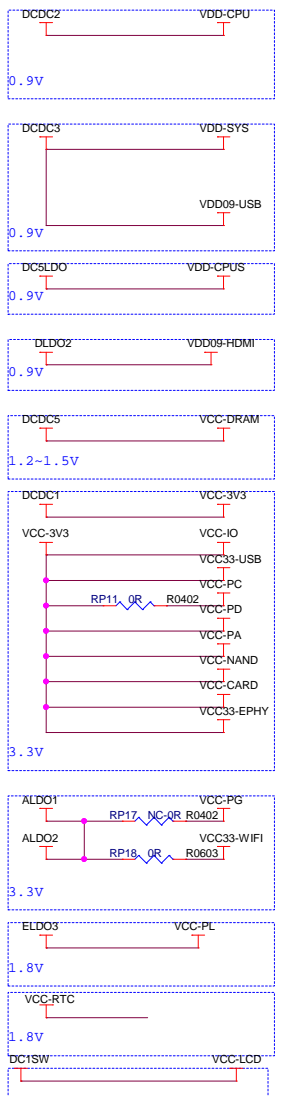
Size: A3 Page Name: CPU Rev: []

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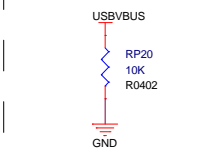
DDR3



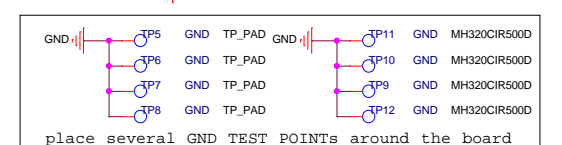
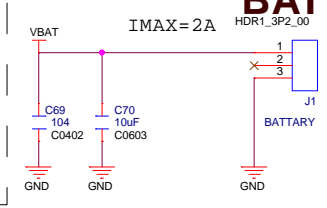
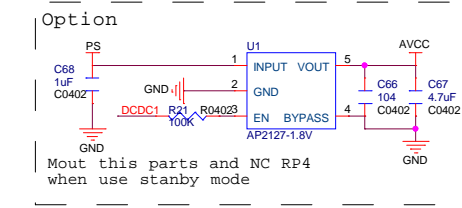
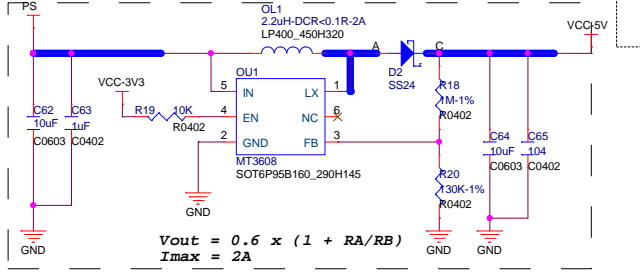
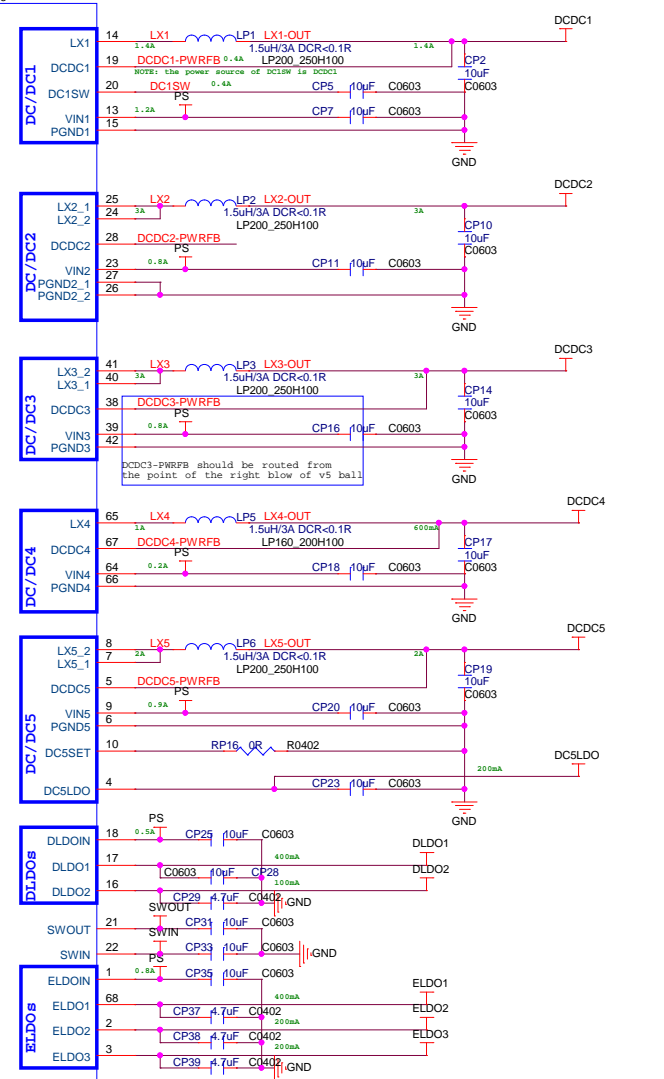
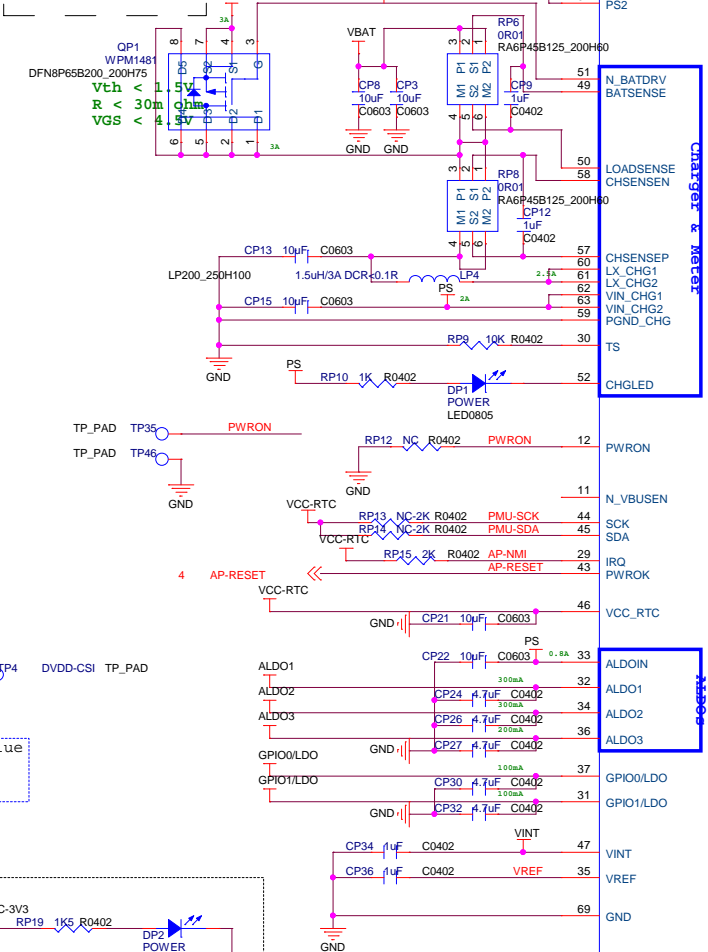
AXP233



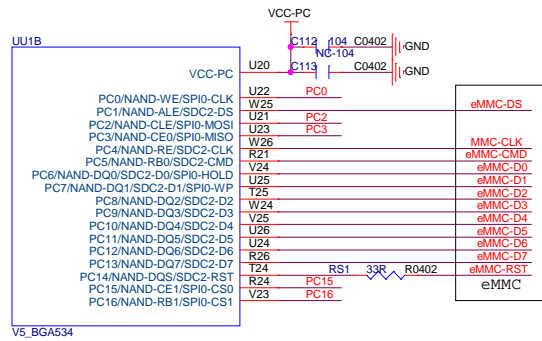
discharge resistor



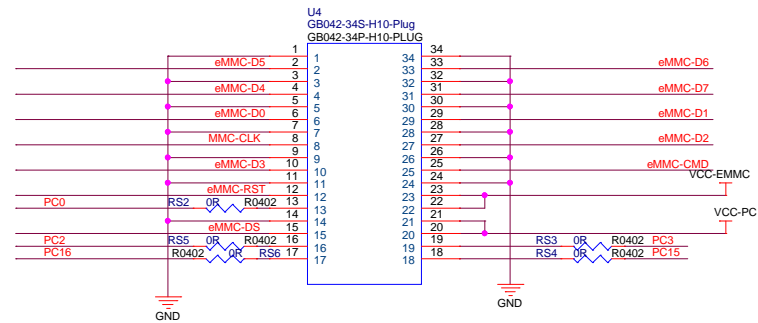
NOTE: 1 mil Copper thickness + 30 mil routing width -> 1A current



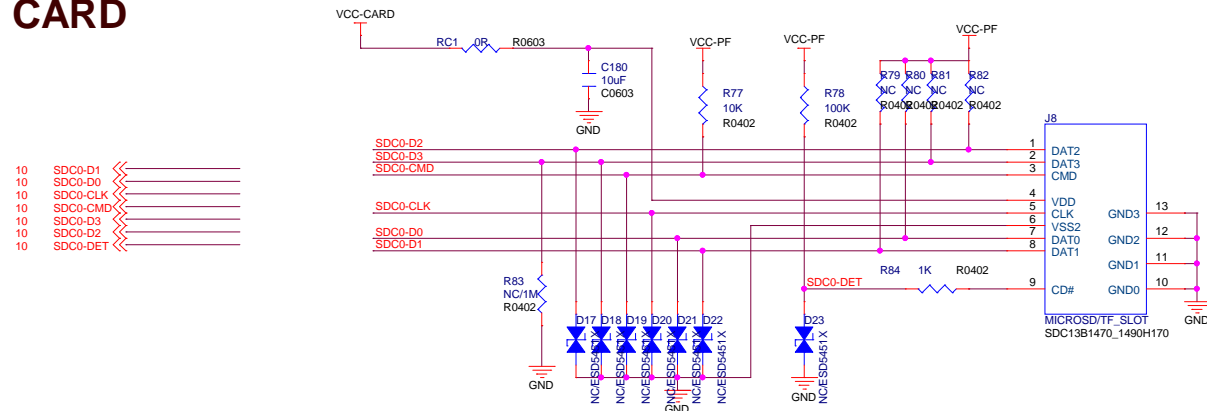
eMMC



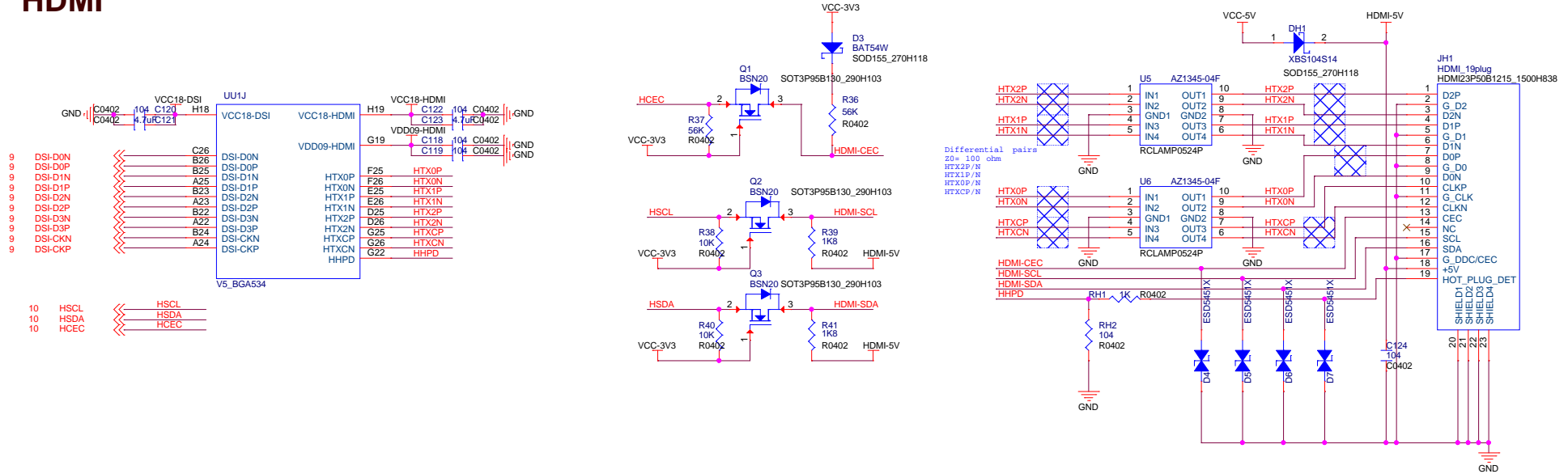
Flash



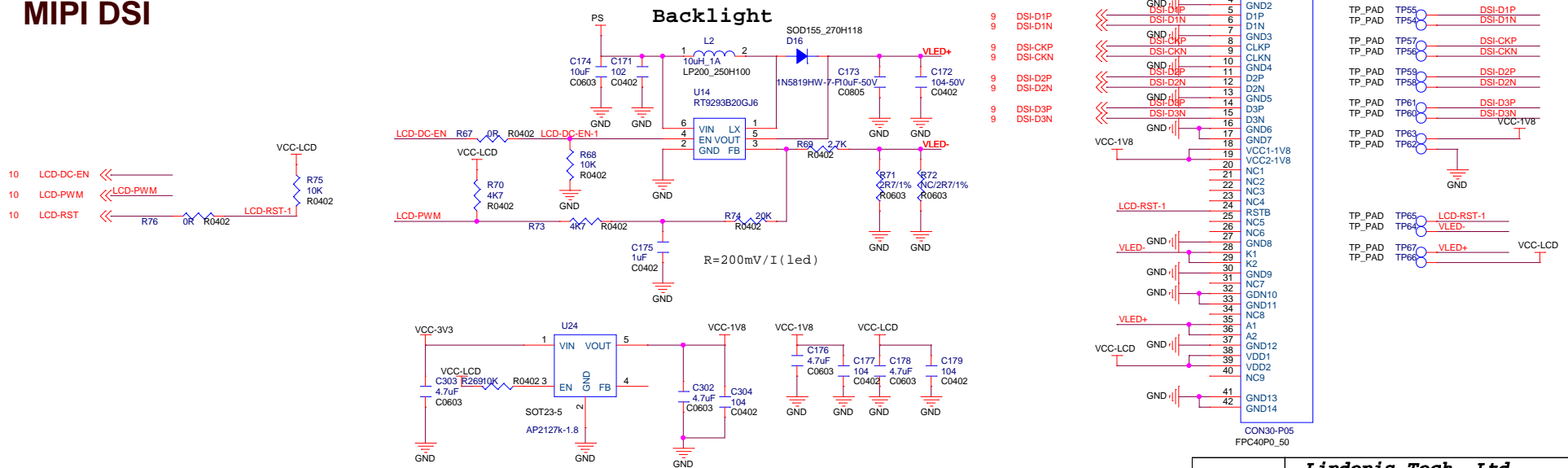
CARD



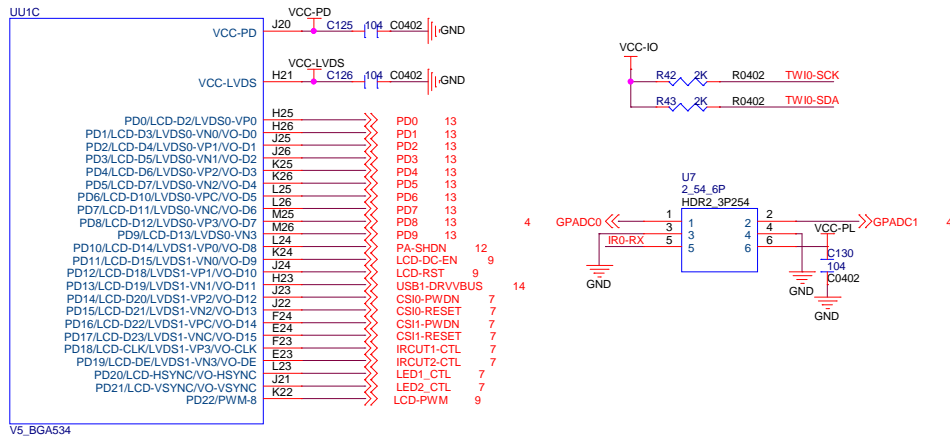
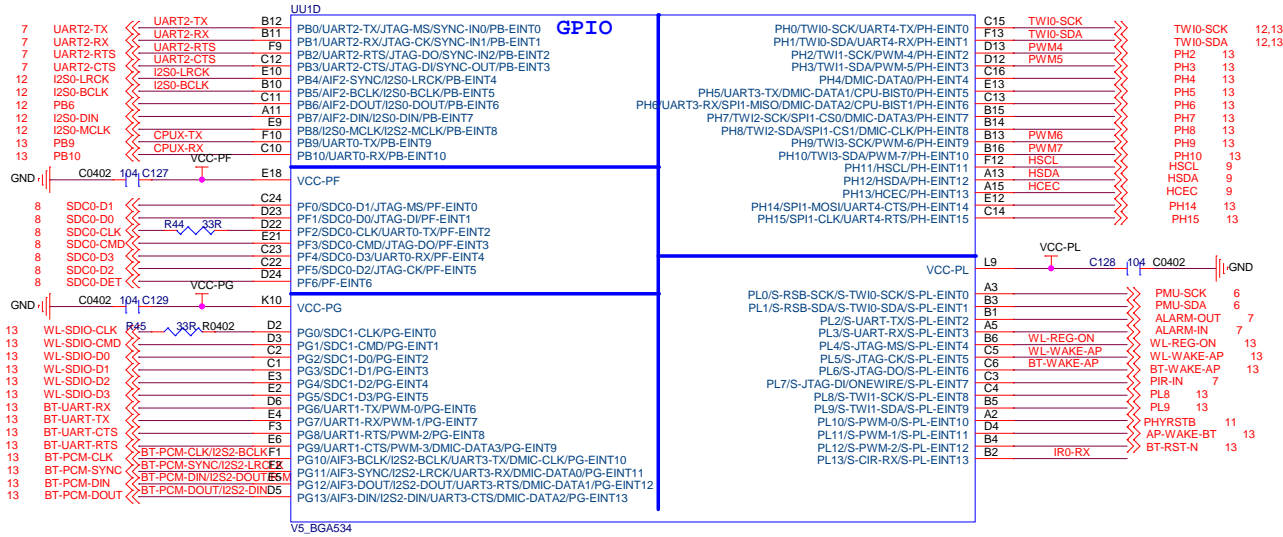
HDMI



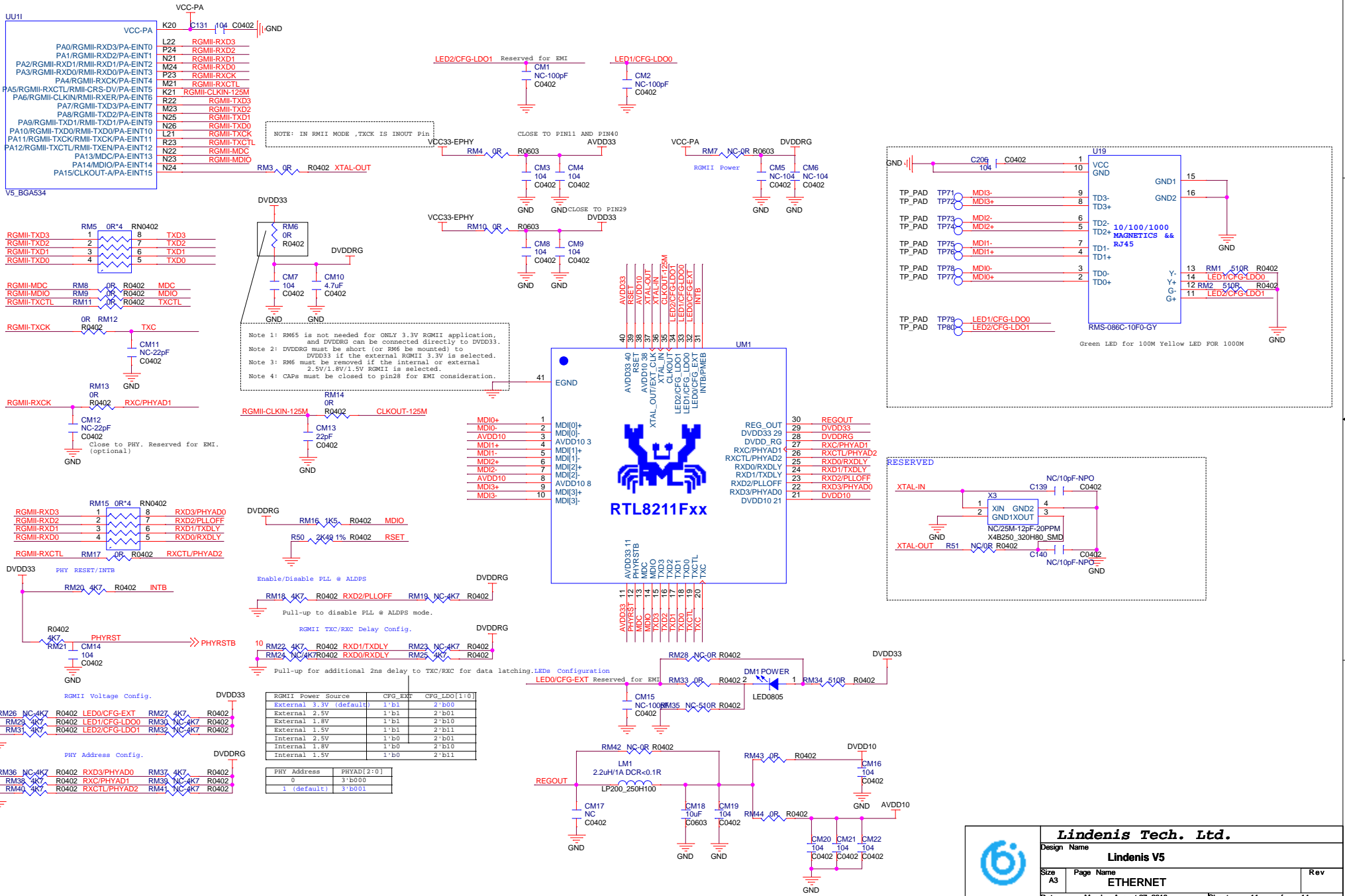
MIPI DSI



GPIOs



ETHERNET



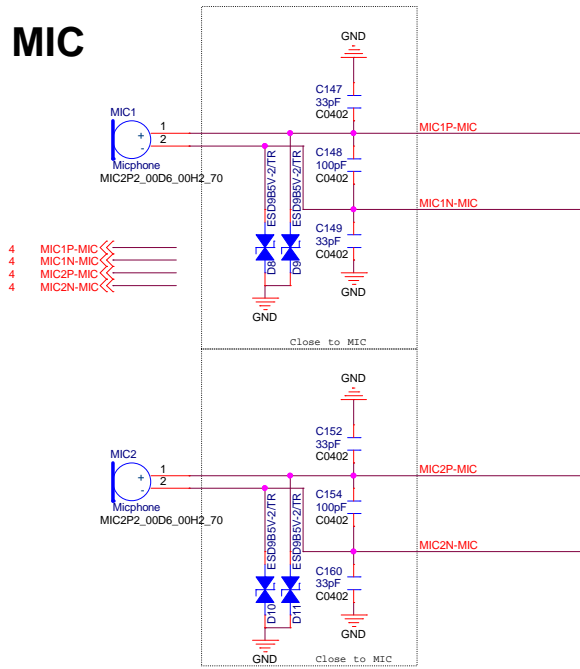
Linden Tech. Ltd.

Design Name: **Linden V5**

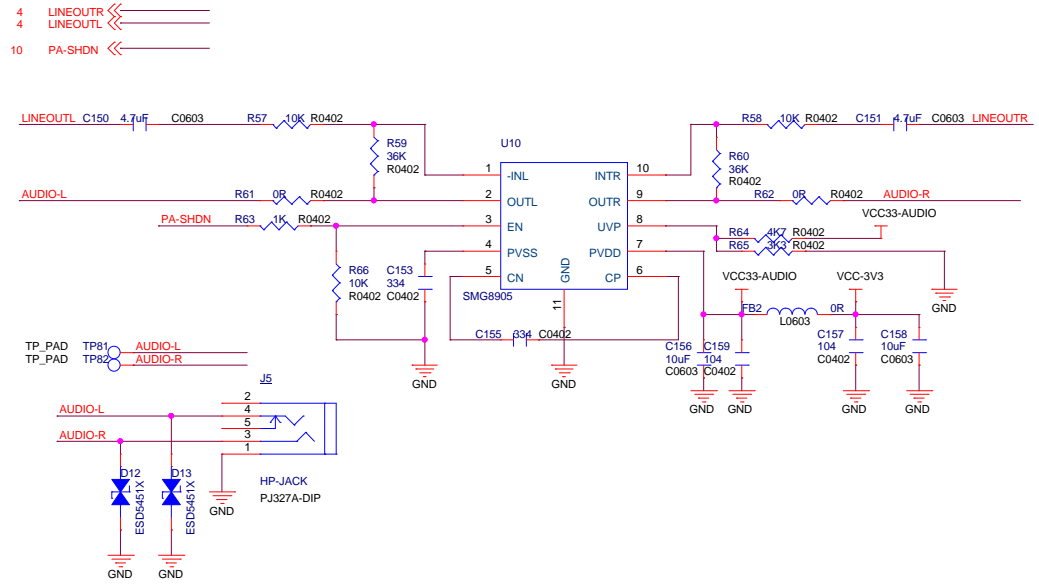
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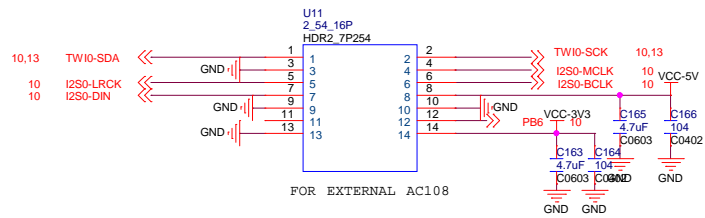
MIC



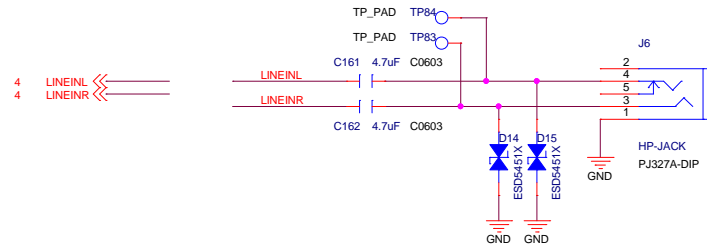
LINE OUT JACK



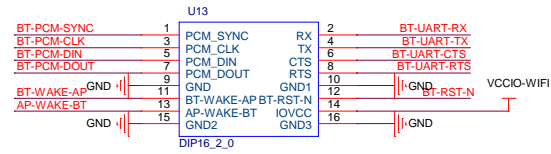
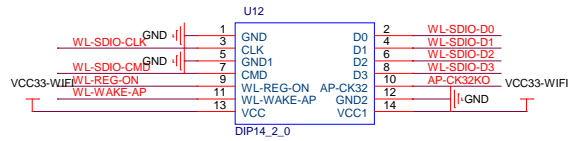
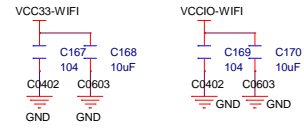
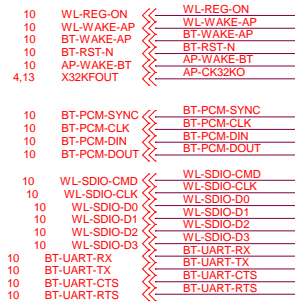
I2S FOR MICs



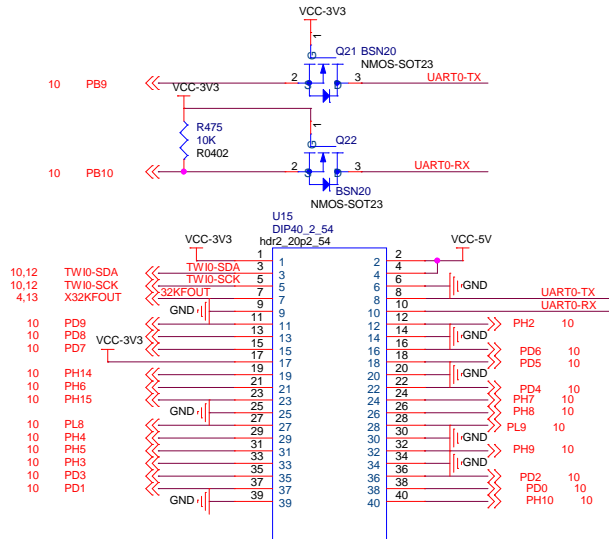
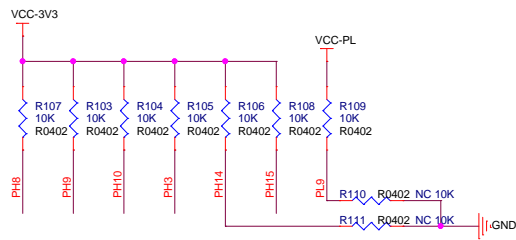
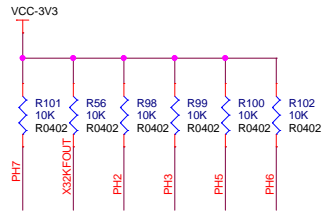
LINE IN JACK



WIFI/BT CON



PI BUS



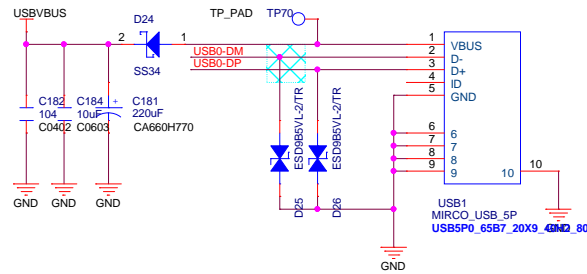
Micro USB

Differential pairs
Z0= 90 ohm

4 USB0-DM
4 USB0-DP

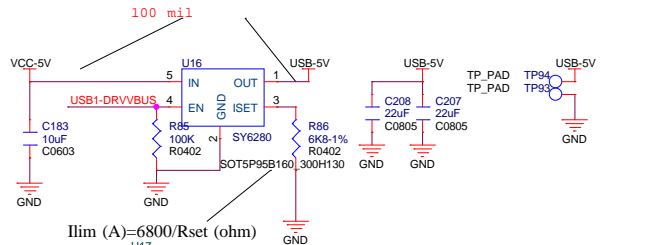
10 USB1-DRVVBUS

TP_PAD TP68 USB0-DM
TP_PAD TP68 USB0-DP



USB HOST*4

4 USB1-DM
4 USB1-DP



$$I_{lim} (A) = 6800 / R_{set} (\text{ohm})$$

