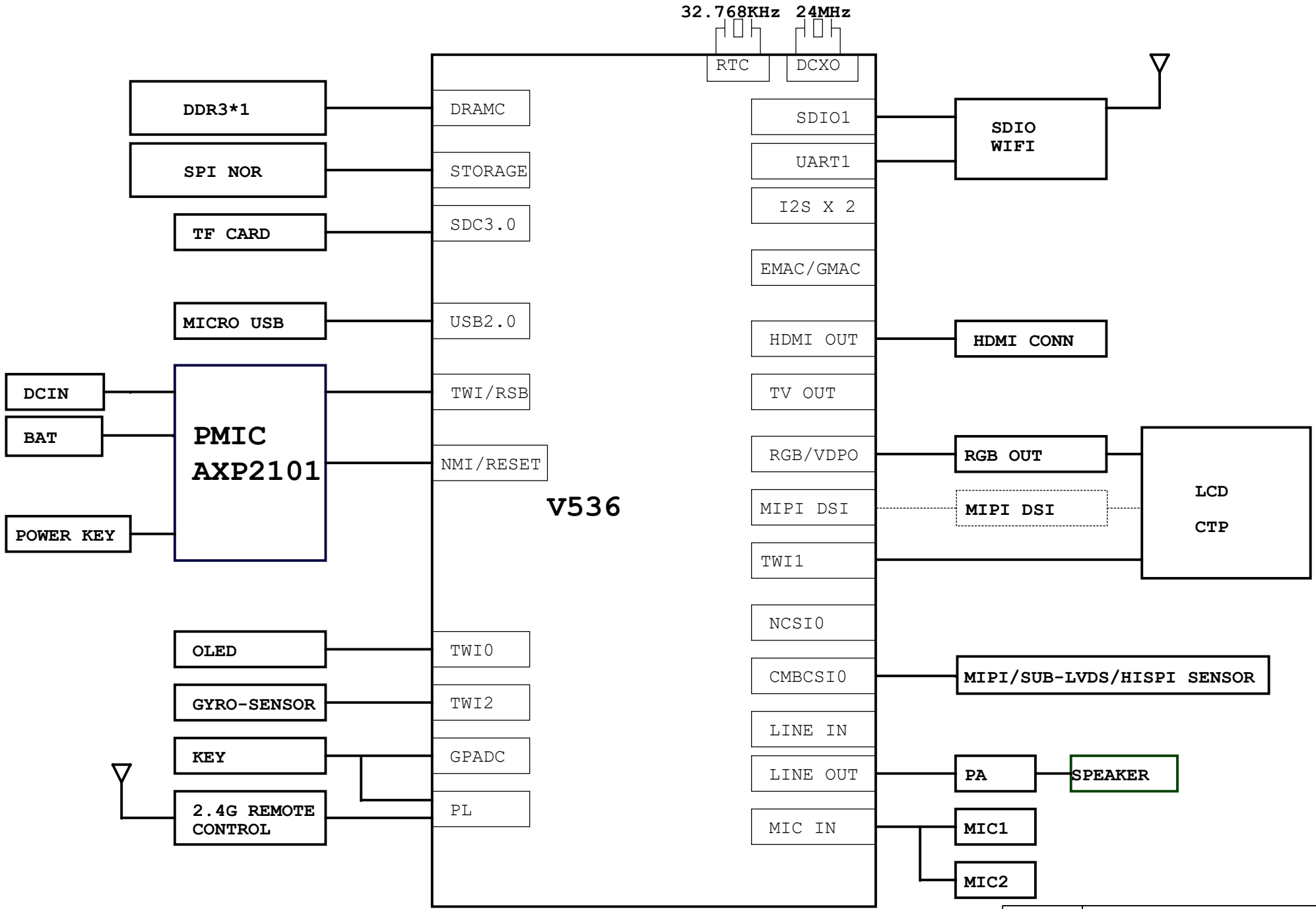
BLOCK



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REVISION HISTORY

Revision	Description	Date	Drawn	Checked
Ver 0.1	Initial Version	2018-08-07	WJW	
Ver 0.2	Changelist	2018-09-07	kJW	
Ver 1.0	1、AUDIO 输入C124电容改为100nF 2、CARD增加预留开关 3、运放电源管脚到滤波电容之间增加R408 1R电阻或120R磁珠，提高抗静电能力 4、VDD-CPUS增加预留对地C339 2.2uF 0402 5、HDMI i2c 串接电阻改为33R	2019-01-02	ZQ	
Ver 1.0	1、VCC33-WIFI VCC-PG 由DCDC5改为DCDC1供电 2、VCC18-WIFI 由ALDO4改为BLDO1供电， 3、VCC18-WIFI 预留DCDC5 供电，提高效率	2019-01-17	ZQ	
Ver 1.0	1、删掉ALDO4电容，删掉VBACKUP电容,删掉DCDC4SW电容 2、BLDOIN网络名改为DCDC1 3、删掉FEL 测试点，PCB注意删掉过孔 4、HDMI i2c 串接电阻改为100R 5、BLDOIN 输入电容CP21改为2.2uF 6、PMU页部分2.2UF电容由0603封装改为2.2uF-0402封装 7、PMU 6PIN和7PIN CP12和CP14合并一个4.7uF 0603封装 8、PMU 6PIN和7PIN CP2和CP10合并一个4.7uF 0603封装	2019-01-21	ZQ	
Ver 1.0	1、删掉PC口R76电阻 2、删掉TV-OUT,改为测试点	2019-01-22	ZQ	

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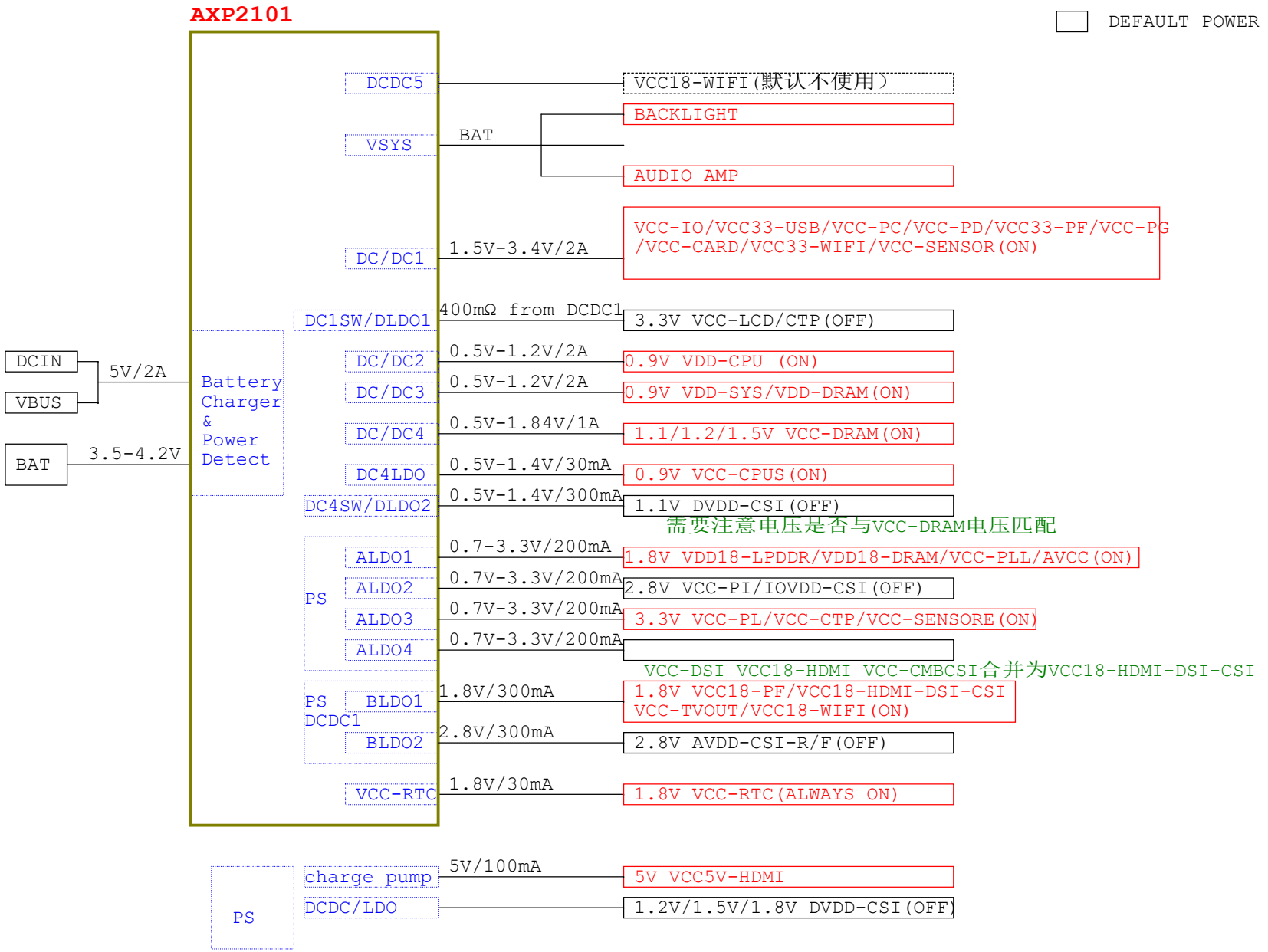
POWER TREE

DEFAULT POWER ON

DEFAULT POWER OFF



1、注意PC口电压
2、注意PE口电压
3、注意PG口电压
4、需要支持USB standby唤醒时，VCC33-USB和VCC-PL合并

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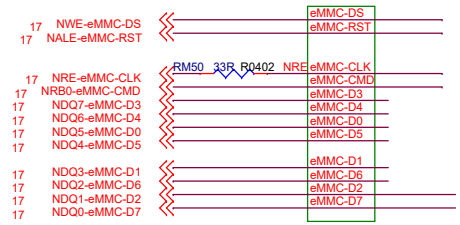
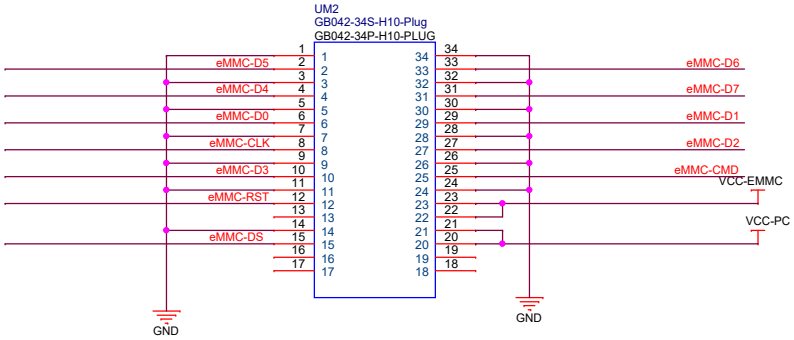
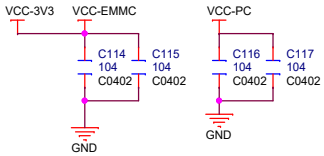
Flash







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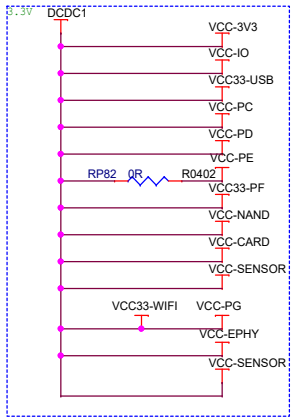
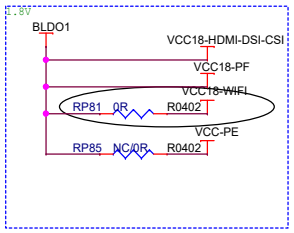
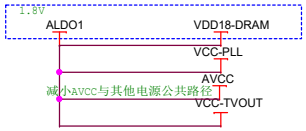
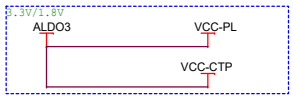
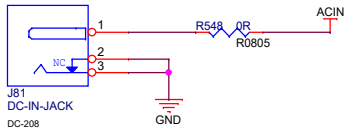


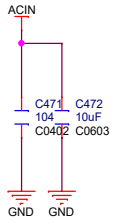




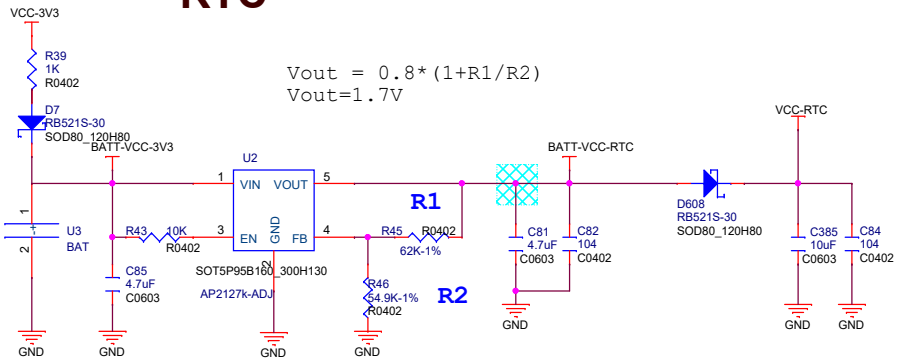


DC-5V





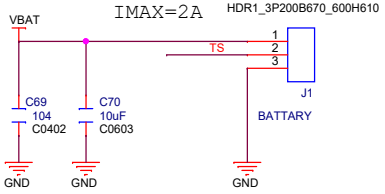
RTC

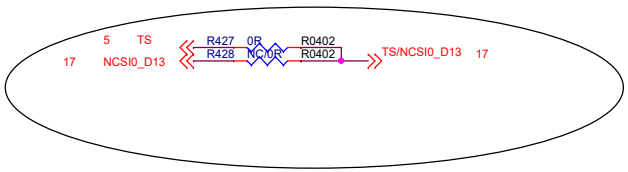


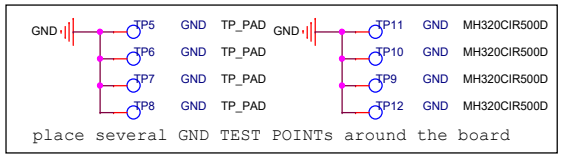
Vout = 0.8*(1+R1/R2)
Vout=1.7V

SINK FROM VINT WHEN PMIC POWER IS PRESENT

BAT







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GPIO ASSIGNMENT

注意：修改电路时请注意同步修改GPIO口说明

PIN	Define	CFG	Function
PC0	SPI0-CLK	4	SPI
PC1	SPI0-CS0	4	
PC2	SPI0-MOSI	4	
PC3	SPI0-MISO	4	
PC4	BOOT-SEL-PC4	7	
PC5		7	
PC6		7	
PC7		7	
PC8		7	
PC9		7	
PC10		7	
PC11		7	
PC12		7	
PC13	SPI0-WP	4	
PC14	SPI0-HOLD	4	

PIN	Define 1	CFG	Function
PD0		7	LCD
PD1	LCD-D3	2	
PD2	LCD-D4	2	
PD3	LCD-D5	2	
PD4	LCD-D6	2	
PD5	LCD-D7	2	
PD6	LCD-D10	2	
PD7	LCD-D11	2	
PD8	LCD-D12	2	
PD9	LCD-RST	1	
PD10	LCD-CS	1	
PD11		7	
PD12		7	
PD13		7	
PD14		7	
PD15		7	
PD16		7	
PD17		7	
PD18	LCD-CLK	2	
PD19	LCD-DE	2	
PD20	LCD-HSYNC	2	
PD21	LCD-VSYNC	2	
PD22	LCD-PWM	2	

PIN	Define	CFG	Function
PE0		7	
PE1		7	
PE2		7	
PE3		7	
PE4		7	
PE5		7	
PE6		7	
PE7		7	
PE8		7	
PE9		7	
PE10		7	
PE11		7	
PE12		7	
PE13		7	
PE14		7	
PE15		7	
PE16		7	
PE17		7	
PE18		7	
PE19		7	
PE20		7	
PE21		7	

PIN	Define	CFG	Function
PF0	SDC0-D1	2	CARD0
PF1	SDC0-D0	2	
PF2	SDC0-CLK	2	
PF3	SDC0-CMD	2	
PF4	SDC0-D3	2	
PF5	SDC0-D2	2	
PF6	SDC0-DET	6	

PIN	Define 1	CFG	Function
PG0	WL-SDIO-CLK	2	WIFI
PG1	WL-SDIO-CMD	2	
PG2	WL-SDIO-D0	2	
PG3	WL-SDIO-D1	2	
PG4	WL-SDIO-D2	2	
PG5	WL-SDIO-D3	2	
PG6			
PG7			
PG8			
PG9	I2S2-MCLK	3	AC101S
PG10	I2S2-BCLK	3	
PG11	I2S2-LRCK	3	
PG12	I2S2-DOUT	3	
PG13	I2S2-DIN	3	

PIN	Define	CFG	Function
PH0	PA-SHDN	1	
PH1	GY-INT	6	
PH2			
PH3	OLED-RES	1	OLED
PH4	OLED-SA0	1	
PH5	LED-WIFI	1	
PH6	LED-SOS	1	
PH7	UART0-TX	3	UART
PH8	UART0-RX	3	
PH9	TWI2-SCK	5	GYRO
PH10	TWI2-SDA	5	
PH11	TWI1-SCK	5	TP SENSOR
PH12	TWI1-SDA	5	
PH13	TWI0-SCK	5	AC101S/OLED
PH14	TWI0-SDA	5	
PH15	LED-REC	1	

PIN	Define1	CFG	Function
PI0			CSI
PI1			
PI2	CMBCSI0-CSI-MCLK	2	
PI3	CMBCSI0-CSI-RSTN	1	
PI4	CMBCSI0-CSI-PWDN	1	
PI5	CSI-SCK	2	
PI6	CSI-SDA	2	

PIN	Define 1	CFG	Function
PL0	RSB-SCK	2	PMIC
PL1	RSB-SDA	2	

PIN	Define 1	CFG	Function
PL2	CPUS-TX	2	S-UART
PL3	CPUS-RX	2	
PL4	KEY-OK	7	
PL5	WL-WAKE-AP	6	
PL6	CTP-INT	6	
PL7	CTP-RST	1	
PL8	S-TWI1-SCK	2	
PL9	S-TWI1-SDA	2	
PL10			
PL11	PL11-SYSEN	1	
PL12	WL-PMU-EN	1	
PL13	IR0-RX	2	IR0-RX

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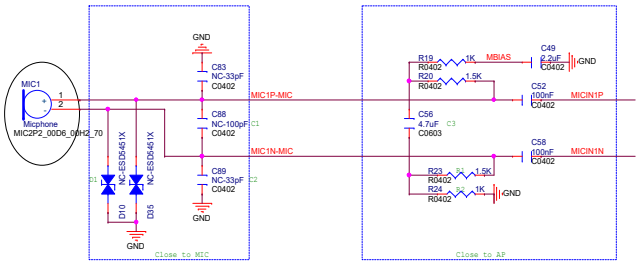
AUDIO

MIC(Electret)



COMPONENT	Diferential	Single-ended
R1 R2 C1 C3 D1	USE	NC
C2	33pF	0R

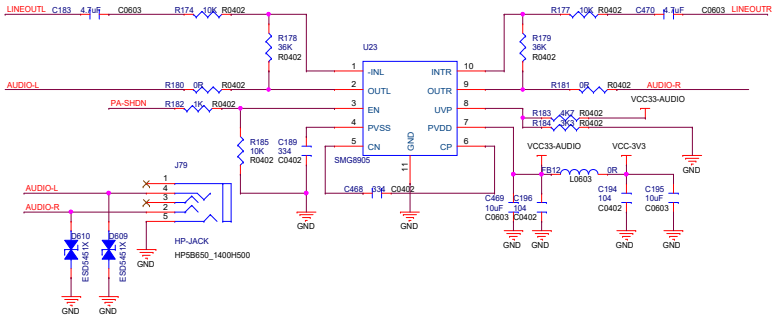
6 MICIN1P
6 MICIN1N
6 MBIAS
6 LINEOUTR
6 LINEOUTL
6 LINEINL
6 LINEINR
7 PA-SHDN

I2S CONN

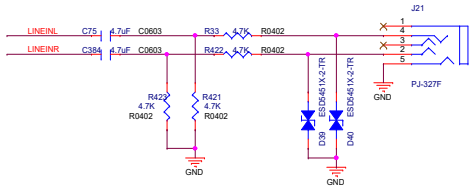
7,15 TWI1-SDA
7,15 TWI1-SCK

16 BT-UART-RTS I2S2_MCLK
16 BT-RST-N I2S2_BCLK
16 WL-PMU-EN I2S2_LRCK
16 AP-WAKE-BT I2S2_DOUT
16 GPS-RESETN I2S2_DIN

SPEAKER



LINE IN



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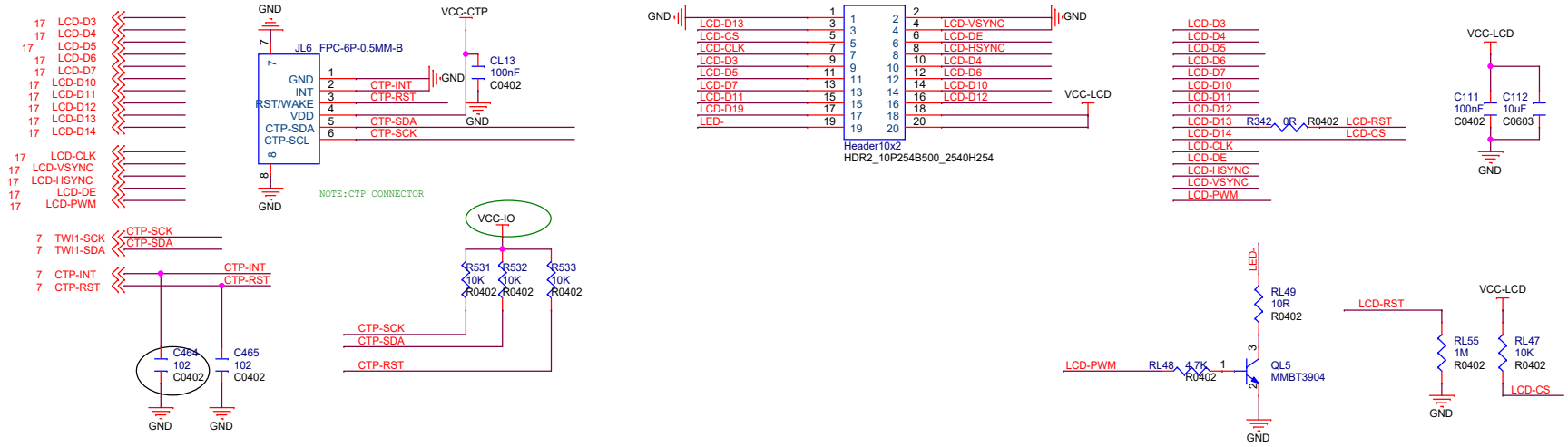
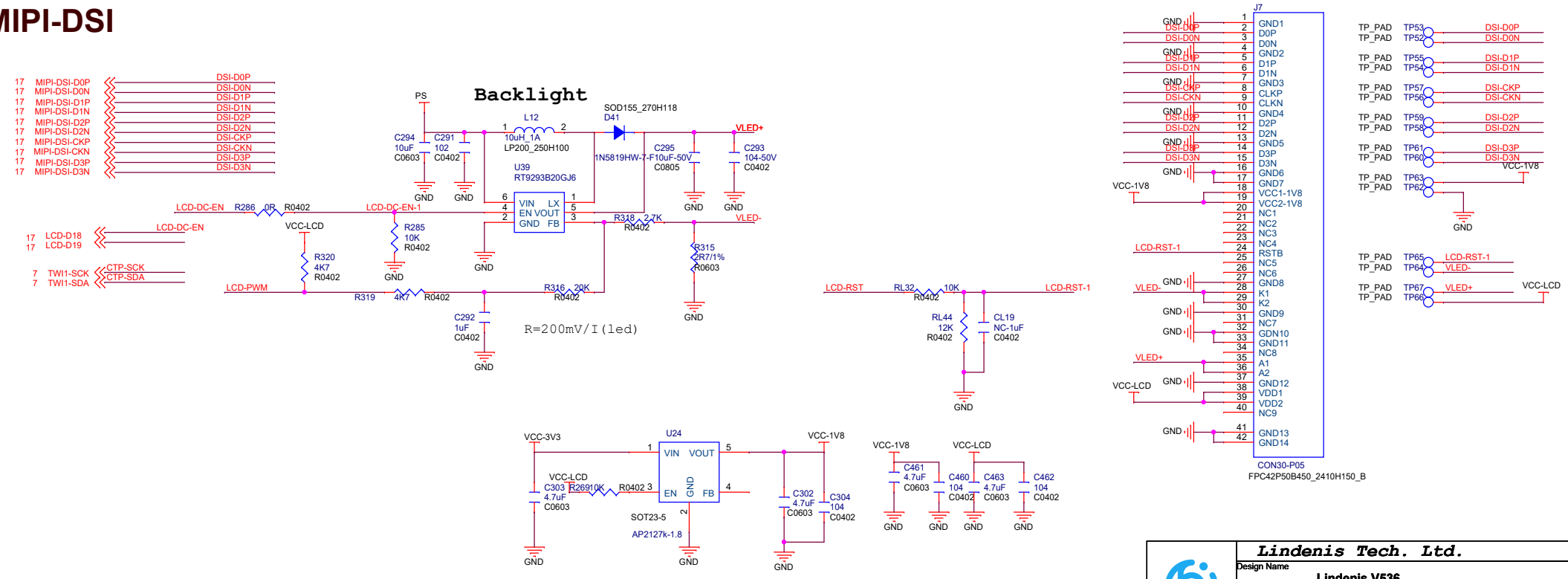
LCD/CTP

CTP CN



MIPI-DSI



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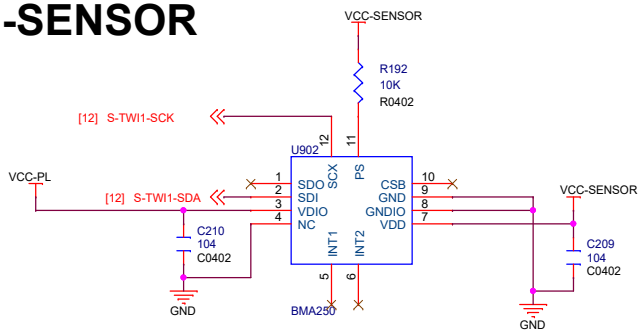
G-SENSOR



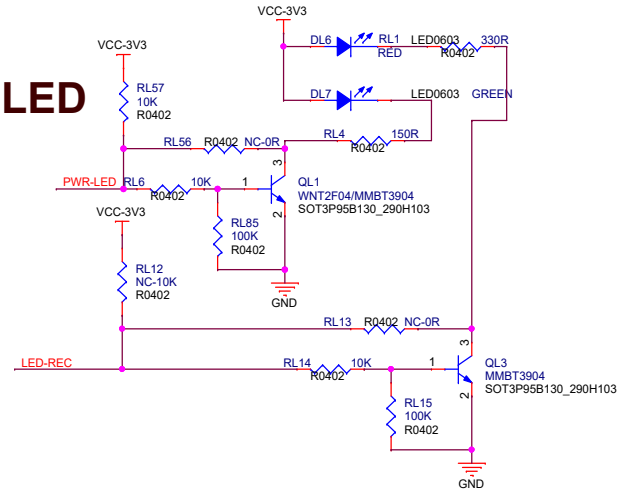
12 S-TWI1-SCK

12 S-TWI1-SDA

7 PWR-LED

7 LED-REC

LED



VCC-3V3

RP84
0R
R0603

AVD33DC

AVD33BAK

3V3-STBY

AVD33DC RP75 0R R0603 AVDUSB

7 GPS-RESETN#

7 UART4_TX

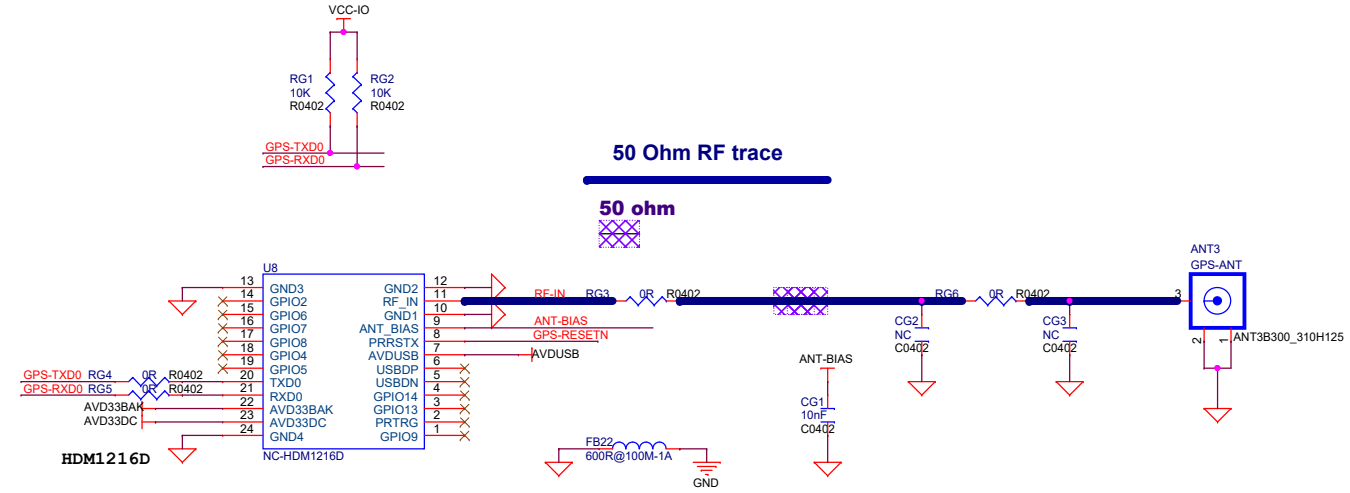
7 UART4_RX

GPS-RESETN

GPS-RXD0

GPS-TXD0

GPS



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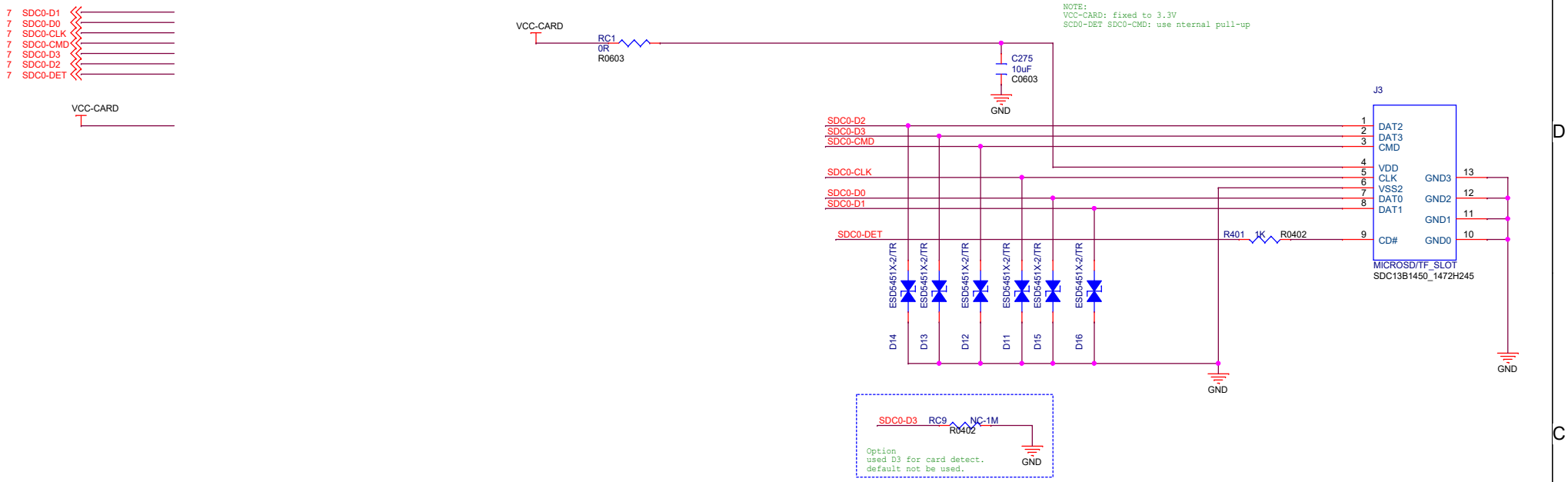
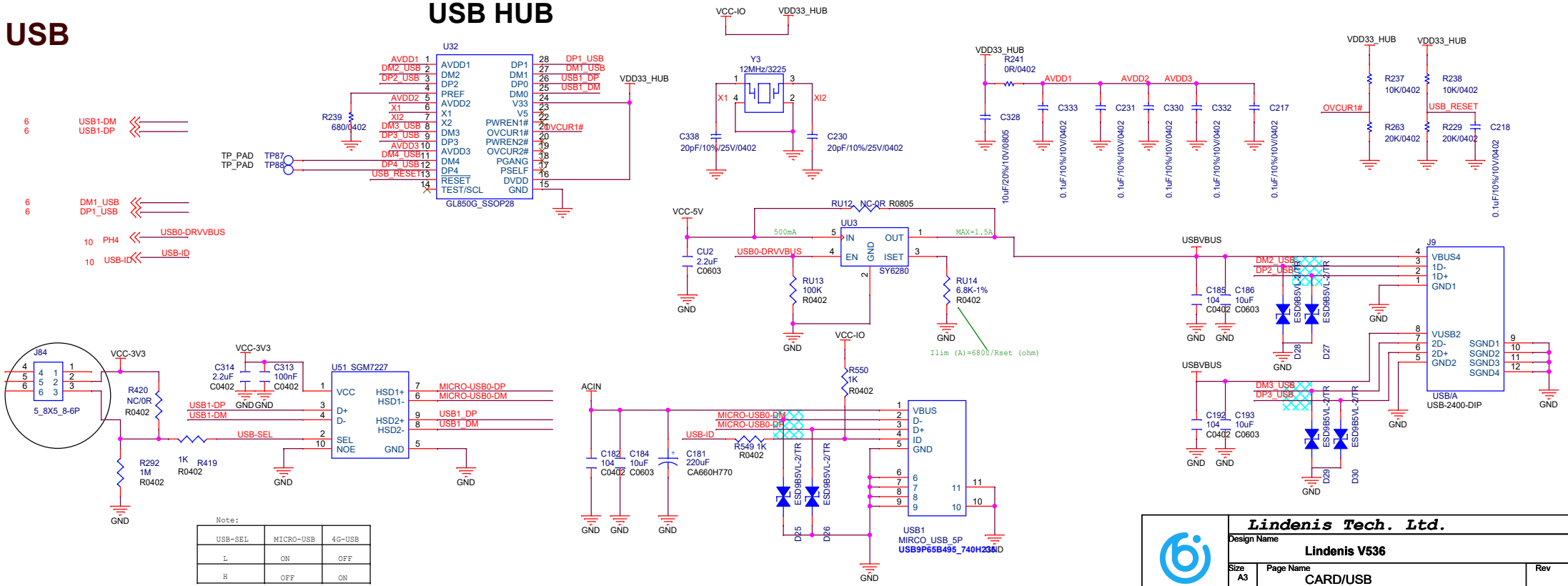
CARD



USB

USB HUB



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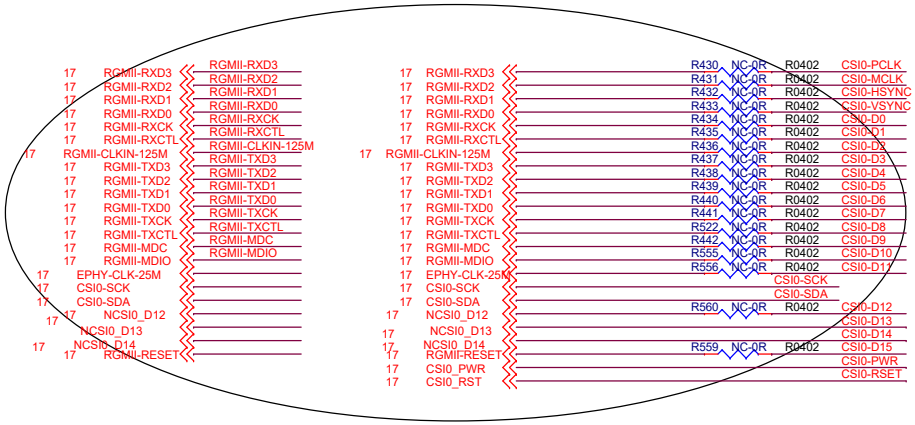
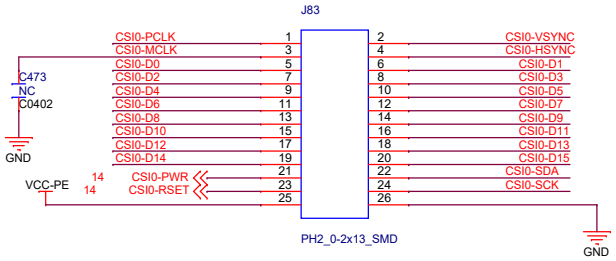
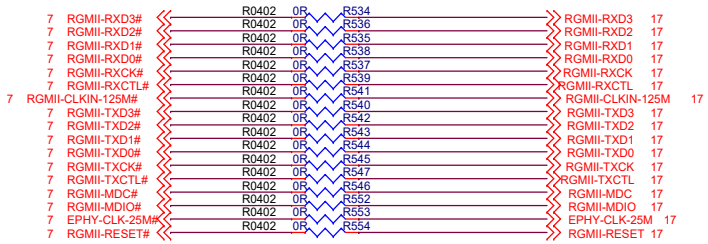
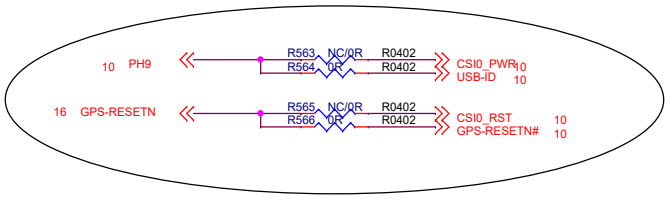
DVP

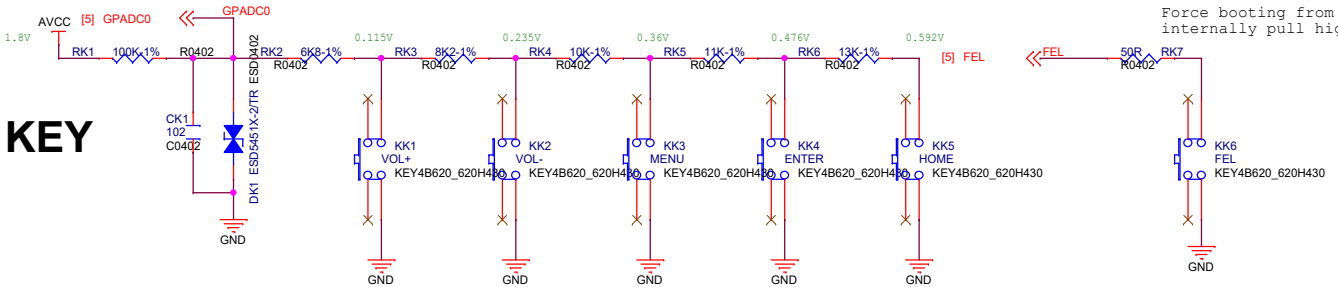




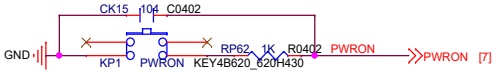


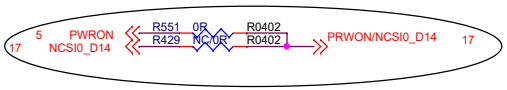


KEY



Force booting from USB0 by pull FEL LOW.
internally pull high





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WIFI

VCC33-WIFI

VCC18-WIFI

7 WL-SDIO-CMD
7 WL-SDIO-CLK
7 WL-SDIO-D0
7 WL-SDIO-D1
7 WL-SDIO-D2
7 WL-SDIO-D3
7 BT-UART-RX
7 BT-UART-TX
7 BT-UART-CTS
7 BT-UART-RTS
6 X32KFOUT — AP-CK32KO

17 BT-RST-N
7,10 WL-WAKE-AP
7,17 BT-WAKE-AP

17 AP-WAKE-BT

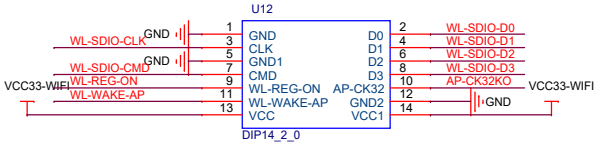
17 WL-PMU-EN — WL-REG-ON

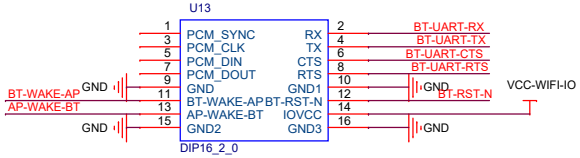
APXXX

VCC18-WIFI — VCC-WIFI-IO

VCC33-WIFI: C167 104 C0402 GND; C466 10uF C0603 GND

VCC-WIFI-IO: C467 104 C0402 GND; C170 10uF C0603 GND





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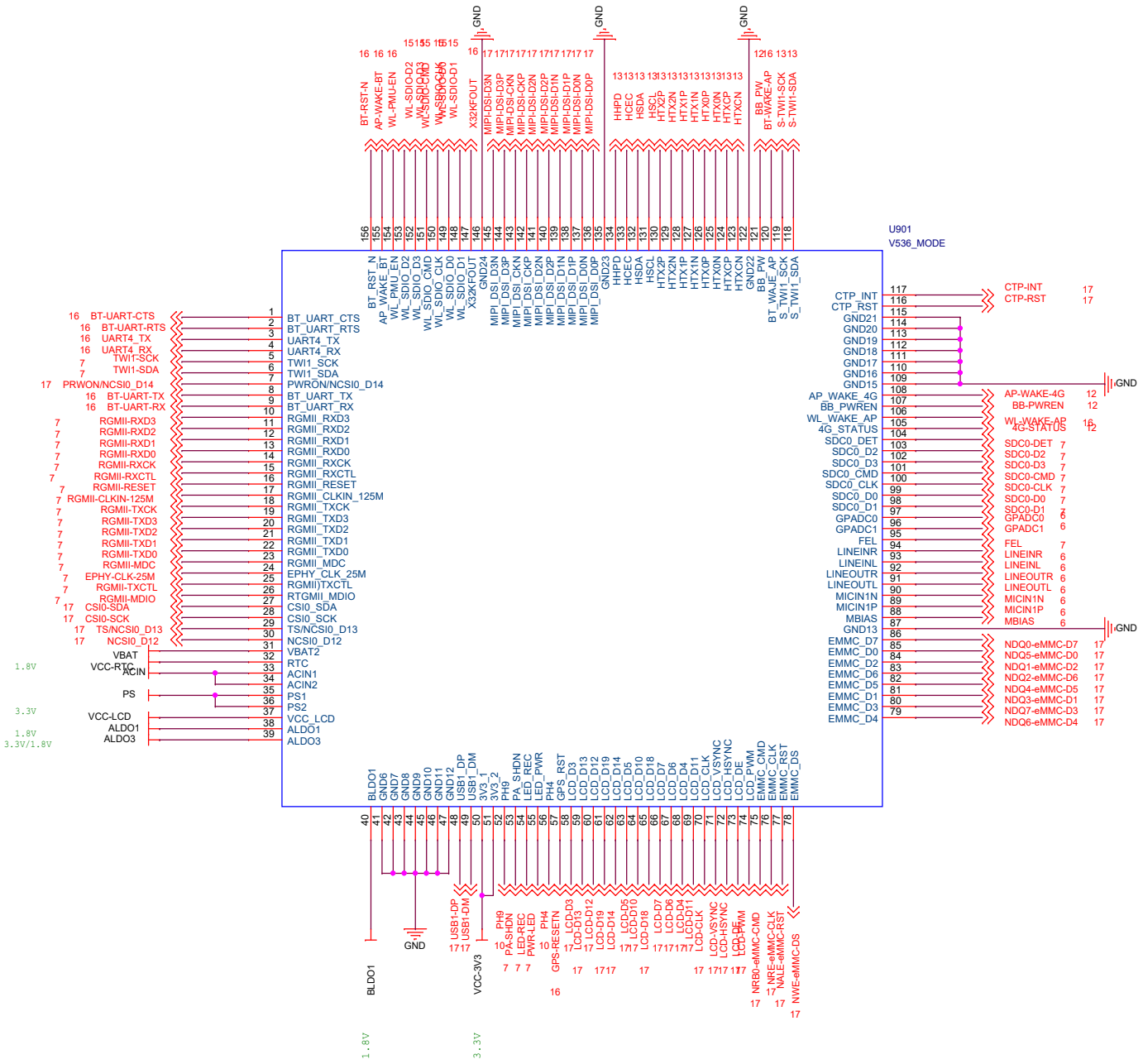

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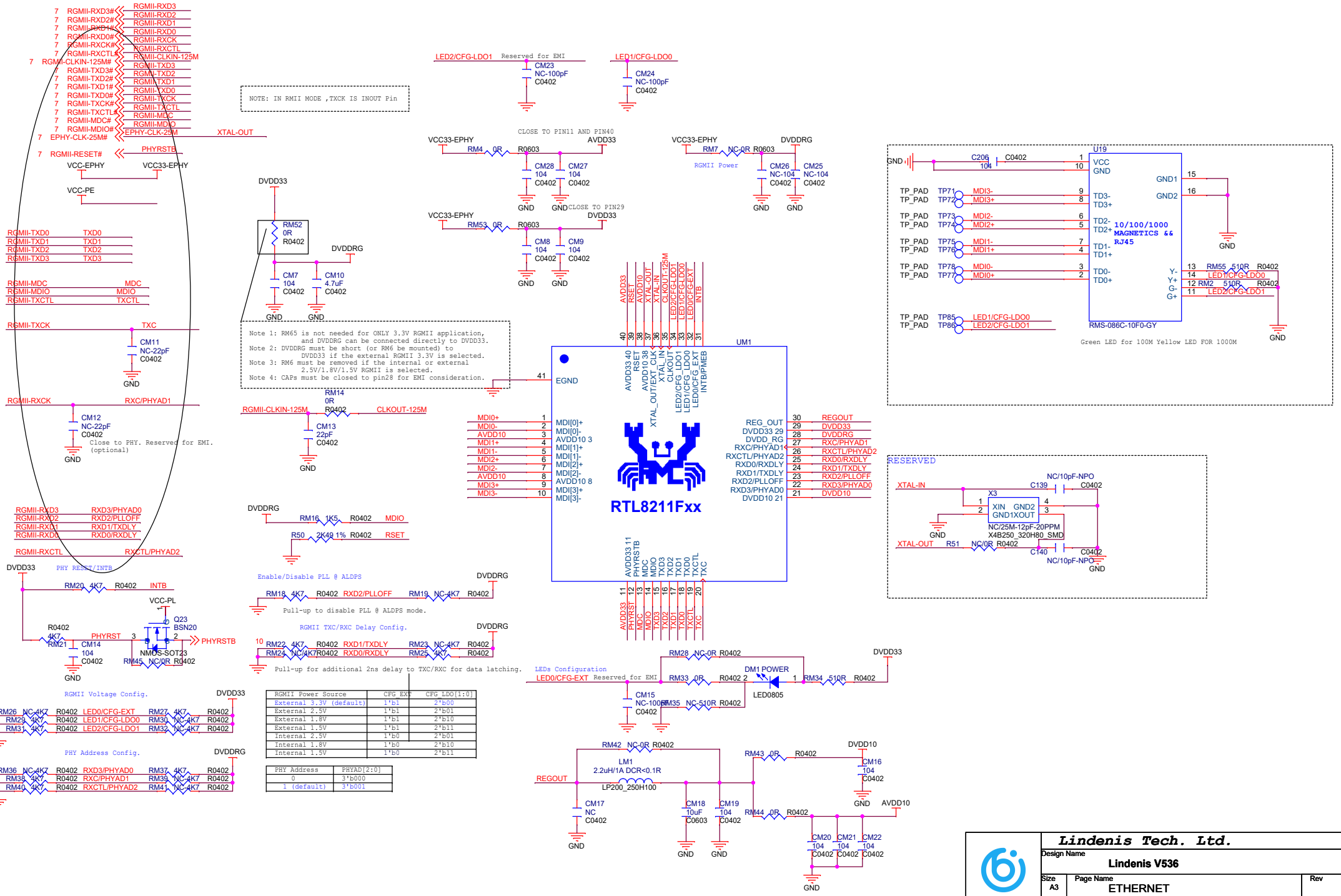
ETHERNET



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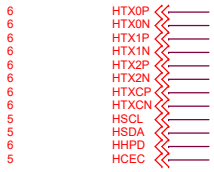
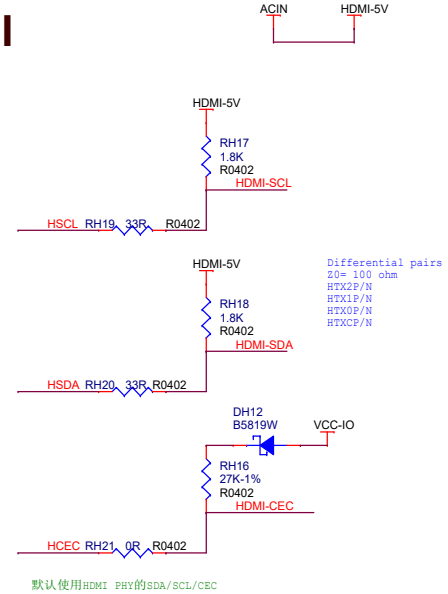
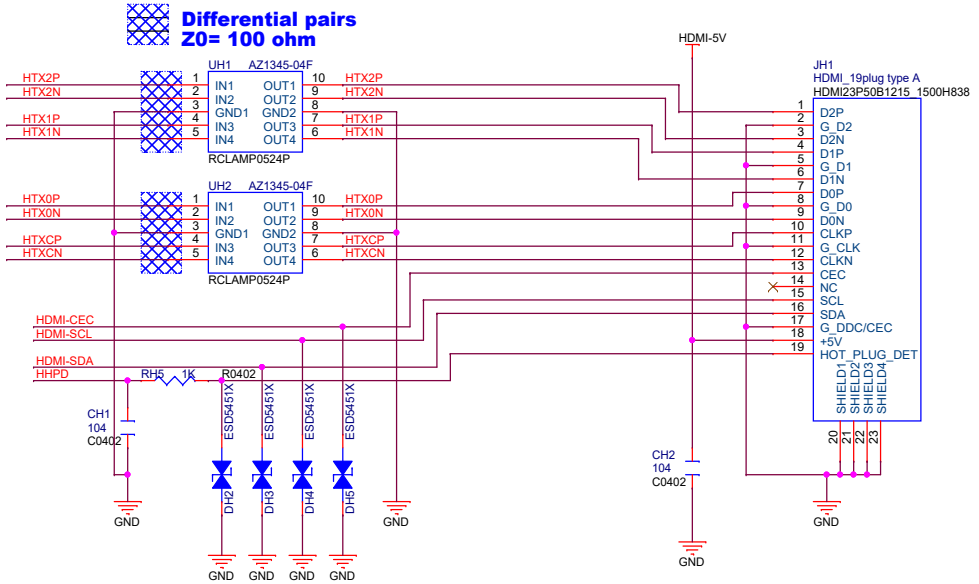
HDMI







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CON

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10 PH9
R567 0R R0402
PH9
PE18

7 TWI1-SCK
7 TWI1-SDA

7 S-TWI1-SDA
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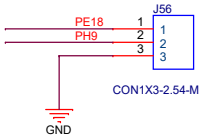
16 UART4_TX
16 UART4_RX

6 GPADC1
6 GPADC0

GPIO



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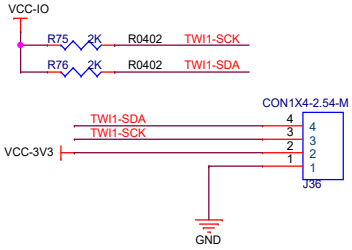

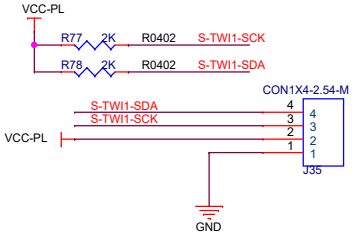

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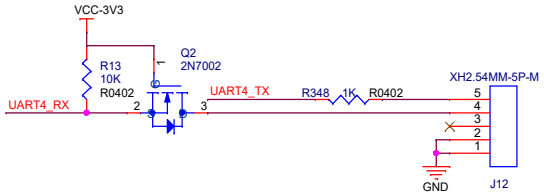

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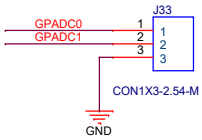

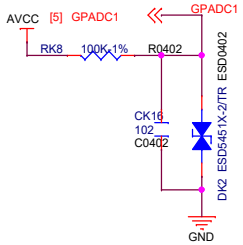

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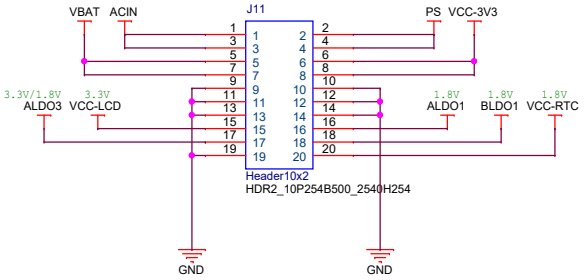

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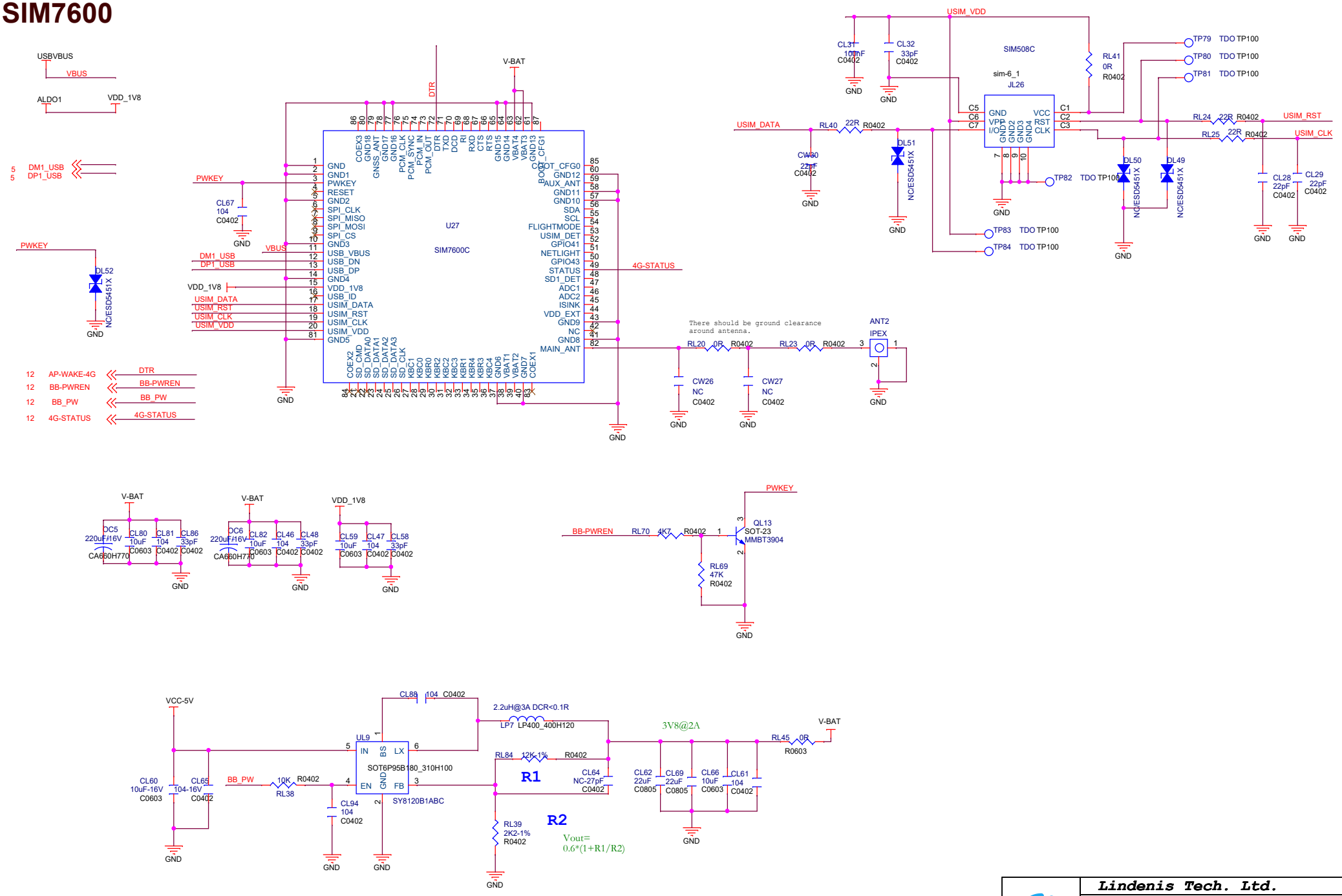
SIM7600



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