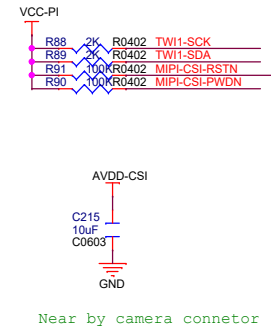
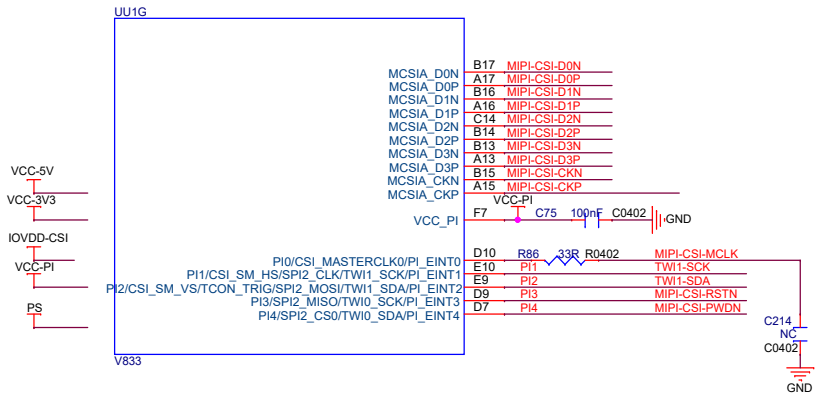


# GPIO ASSIGNMENT

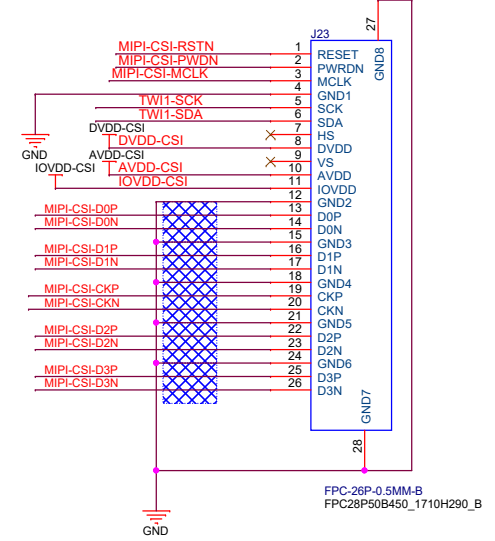




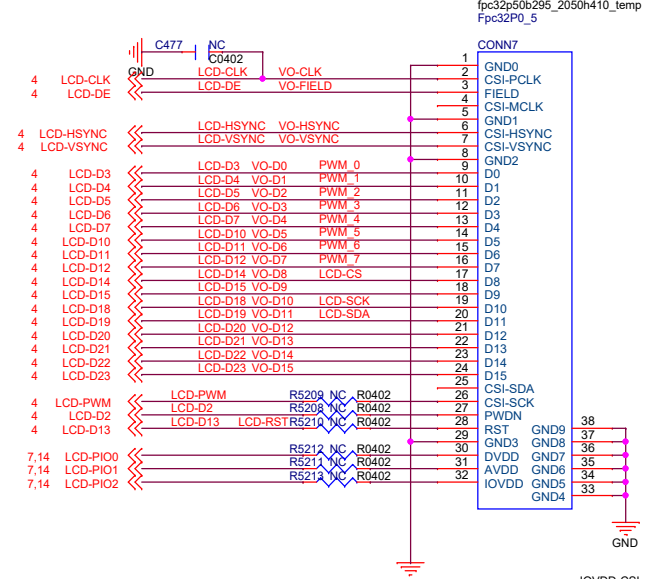




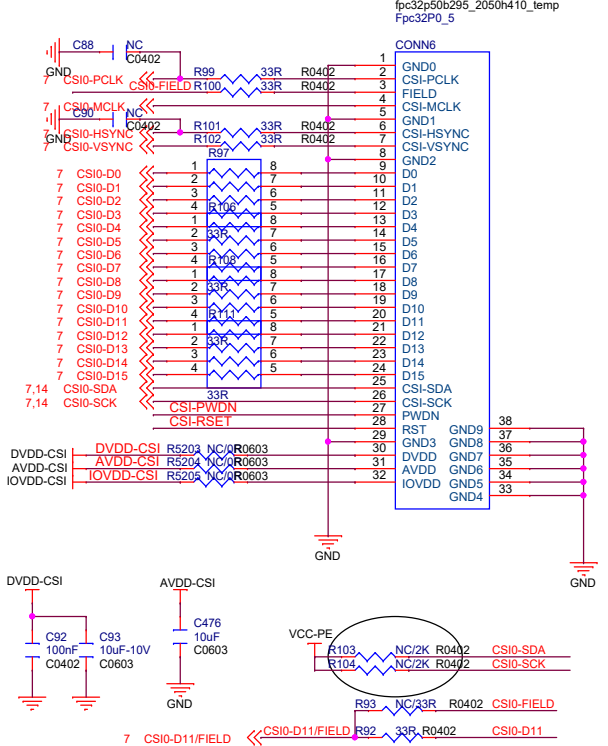
接IMX278子板



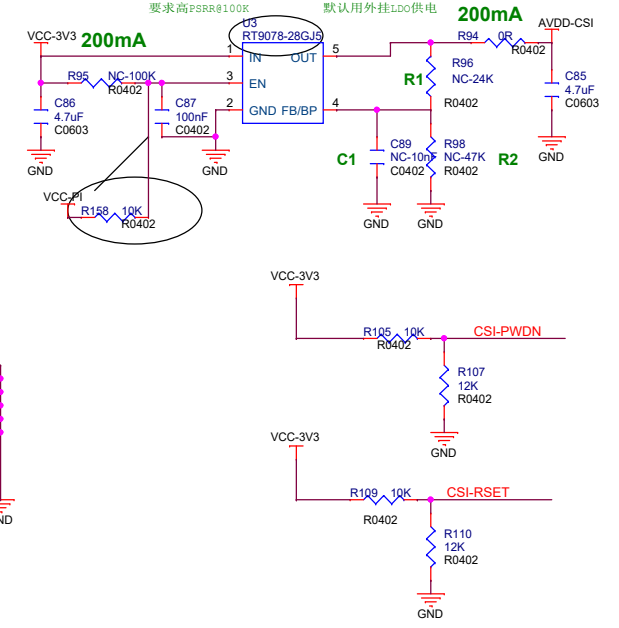
BT1120 OUT



BT1120 IN



前置摄像头AVDD供电2.9V

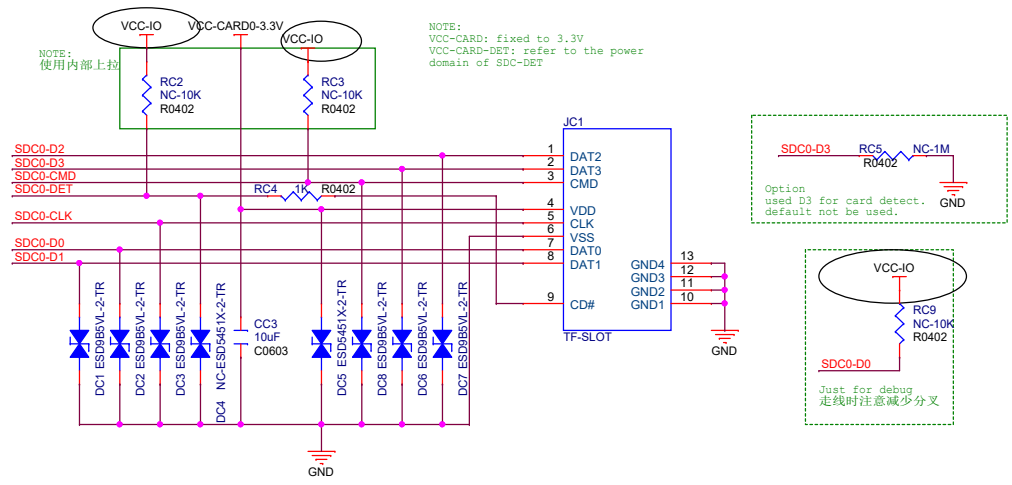
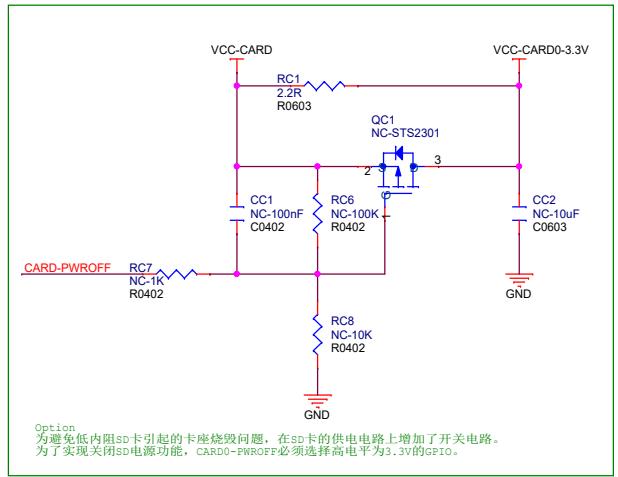




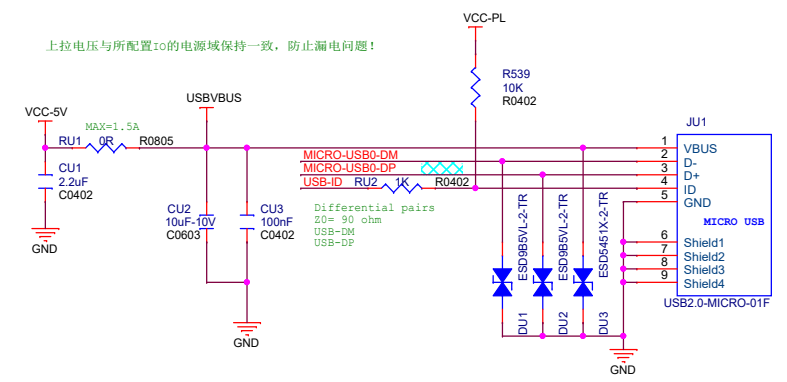
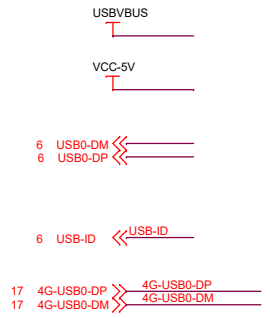




# CARD

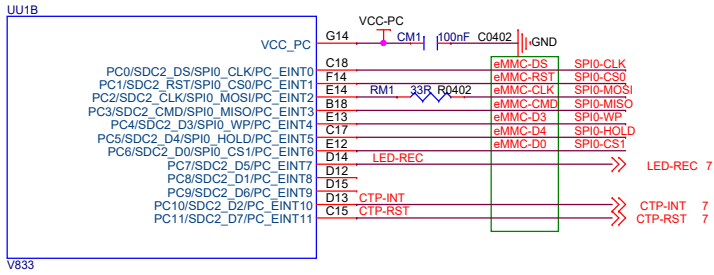


# microUSB

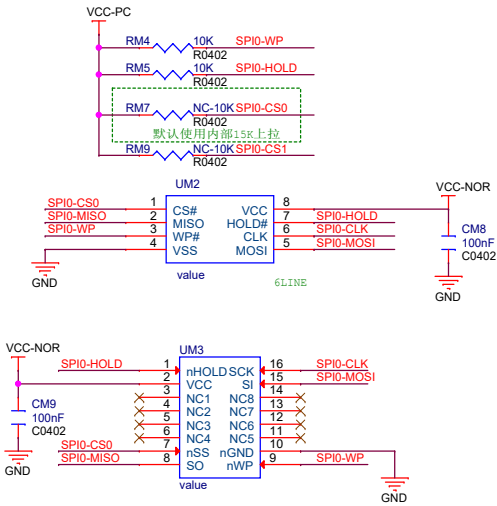


note: Make sure the routing between the ESD and the USB connectors should be on the same PCB side

# FLASH

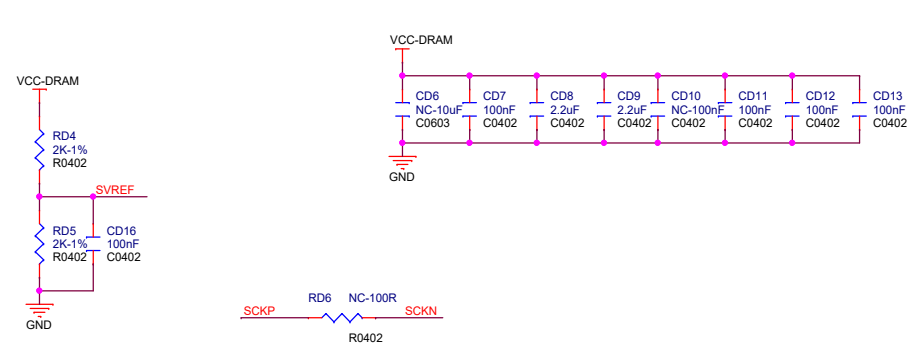
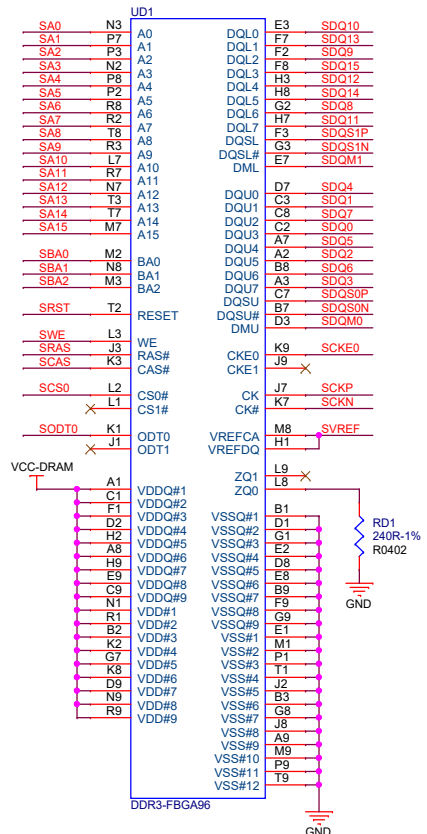
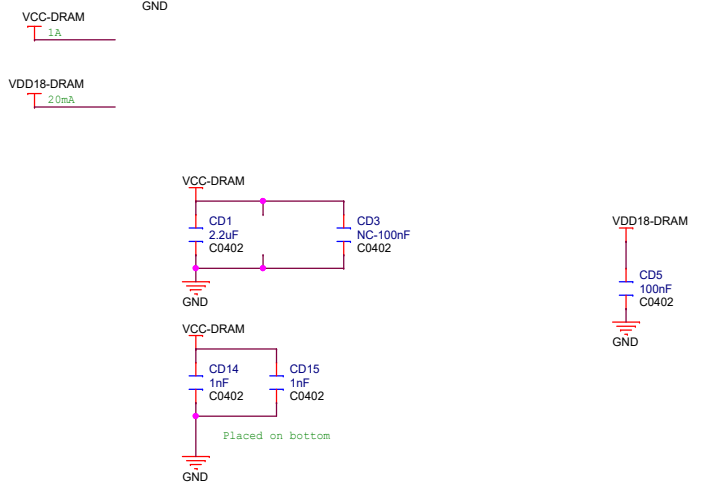
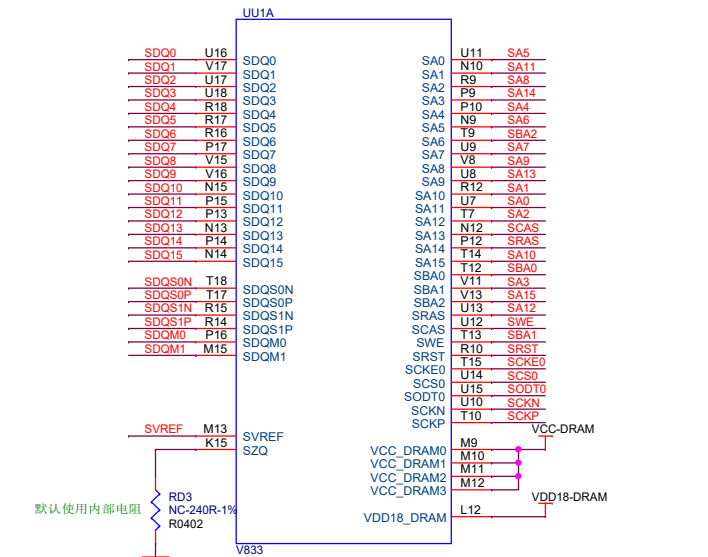


## PC-SPI0

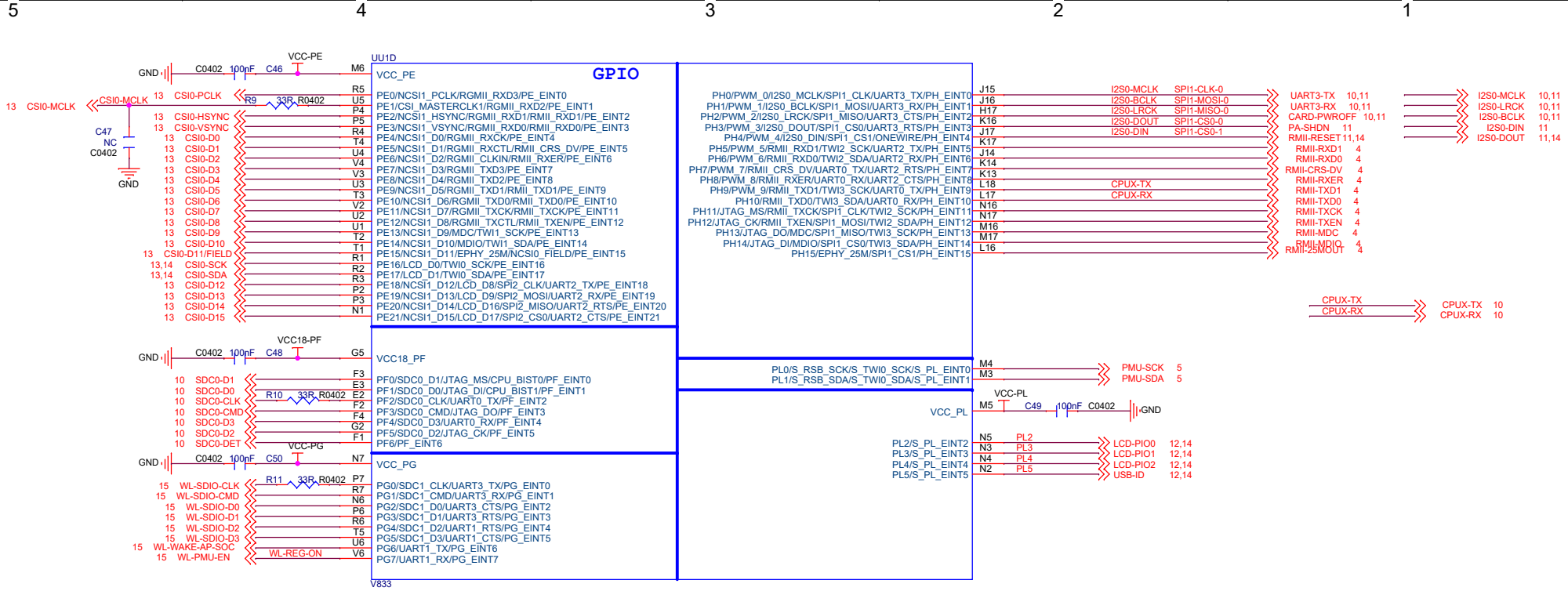




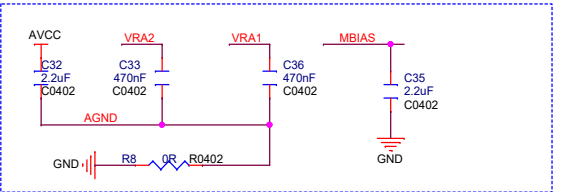
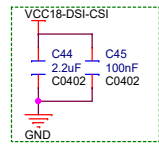
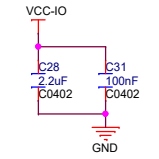
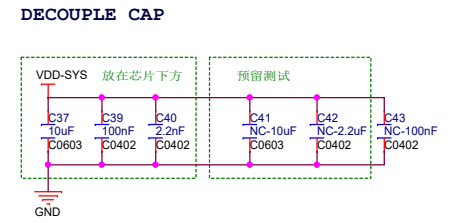
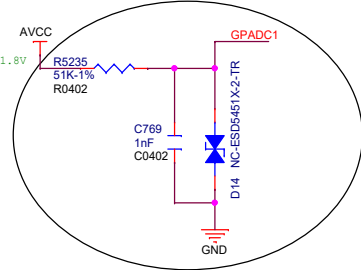
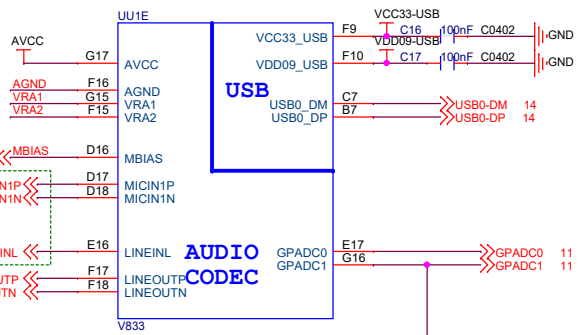
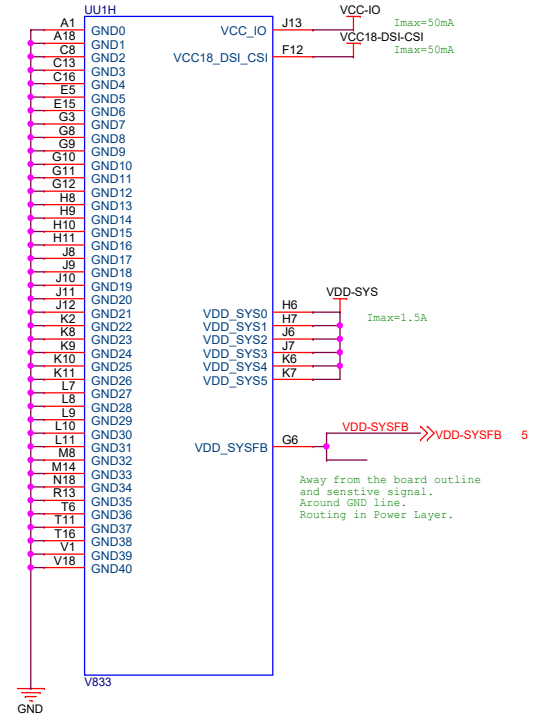
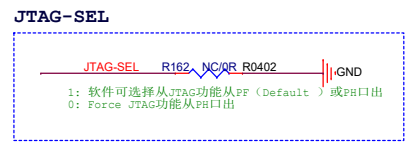
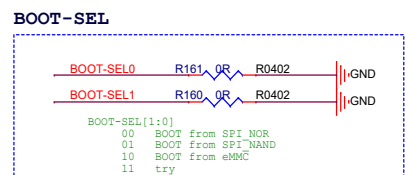
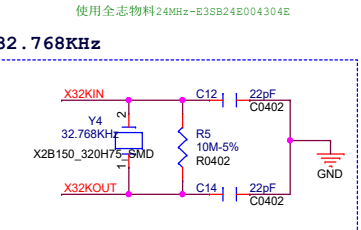
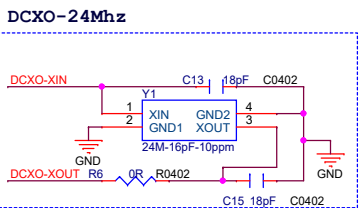
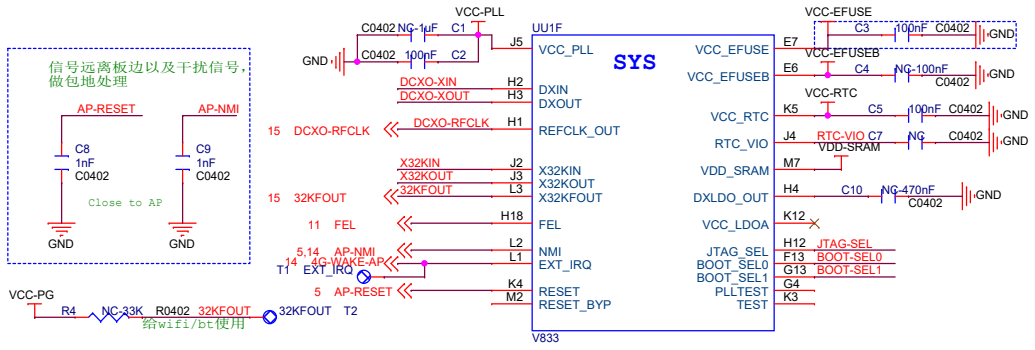
# DDR3 16X1



# SOC

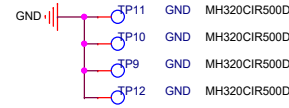
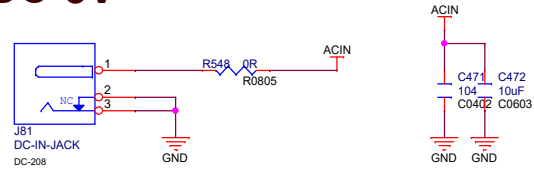


OR 电阻默认贴上, 验证时可将其他模块电阻去掉

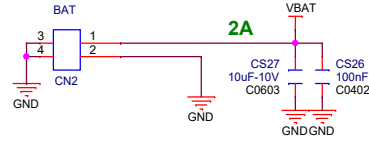


			<b>Lindenis Tech. Ltd.</b>		
			Design Name <b>Lindenis V833</b>		
Size A3	Page Name CPU	Rev			
Date:	Wednesday, April 21, 2021	Sheet	11	of	14

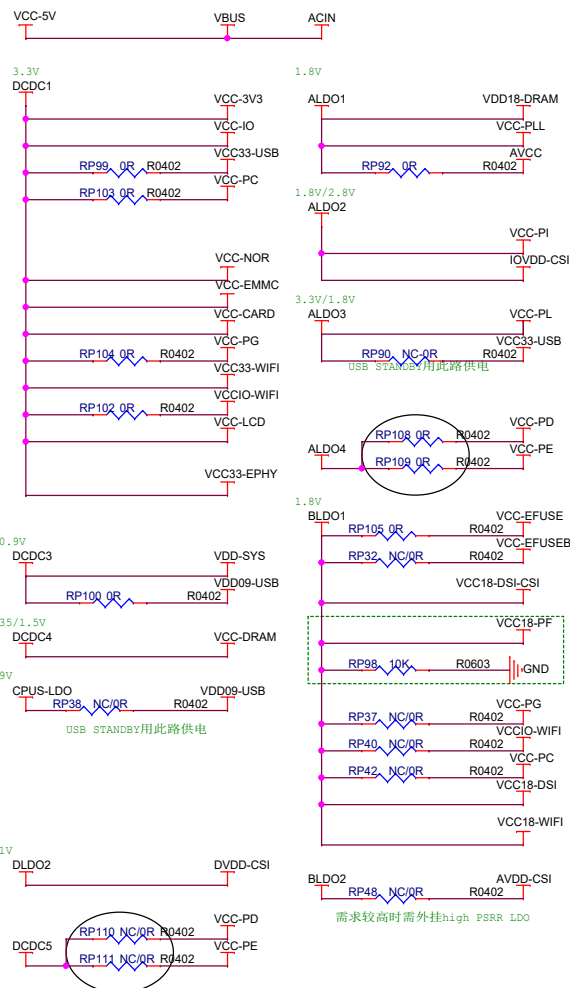
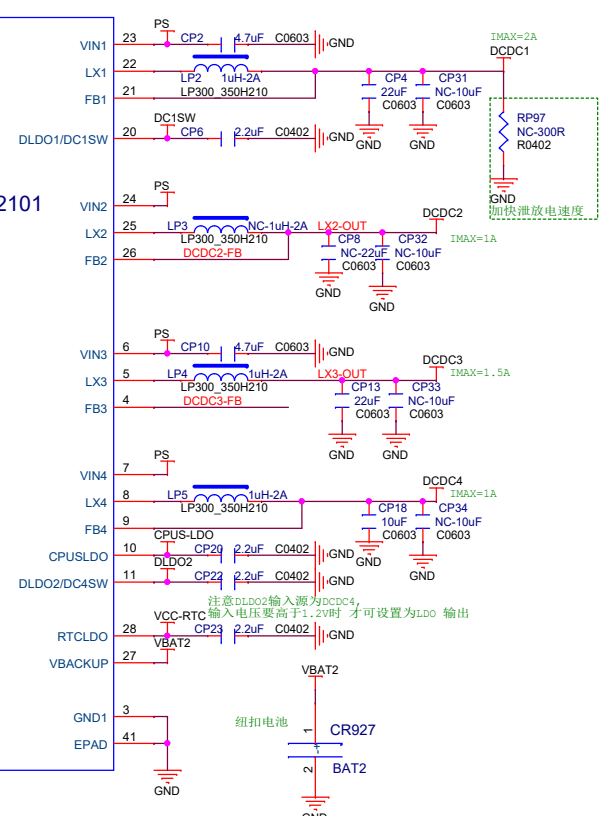
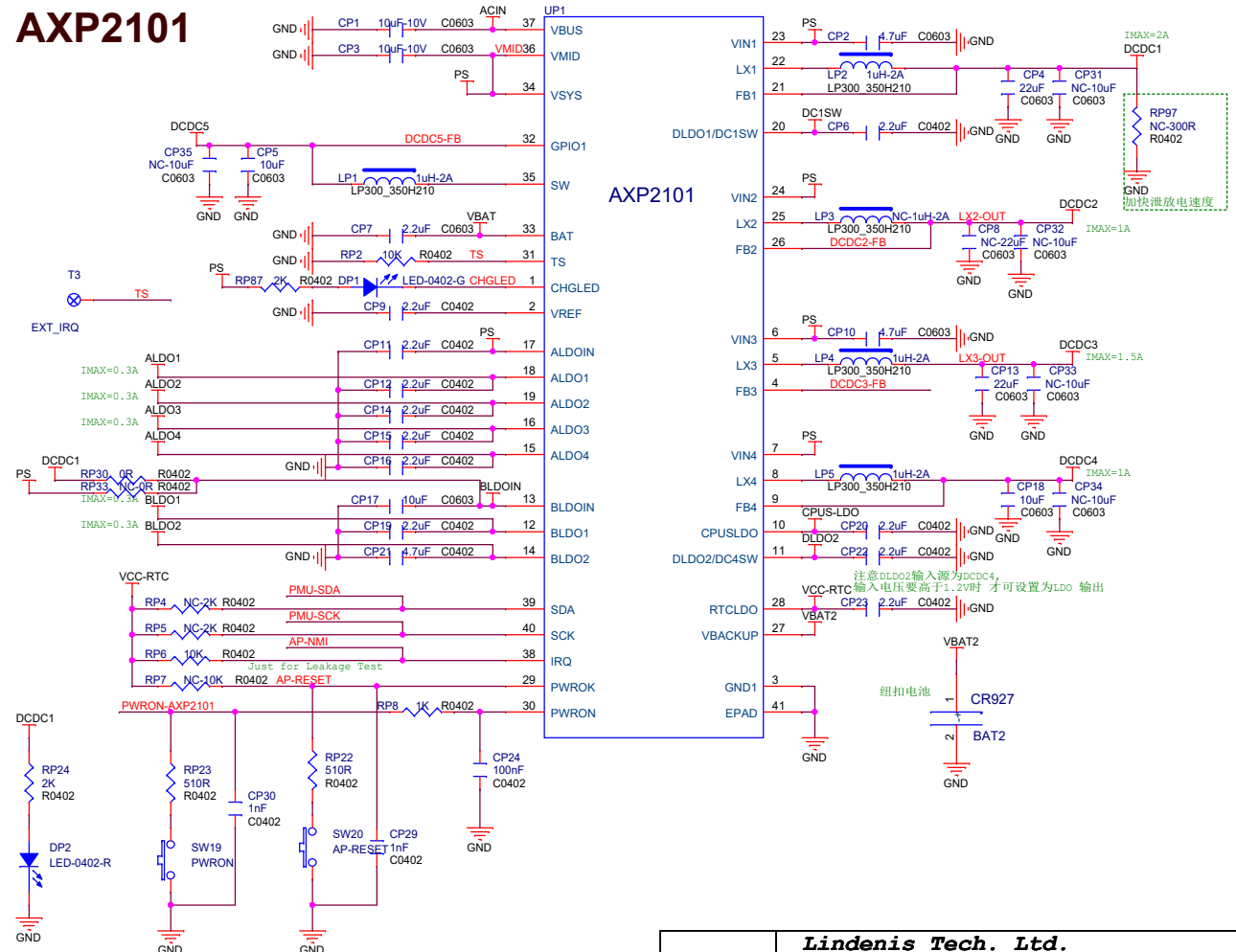
# DC-5V



# BAT IN



# AXP2101

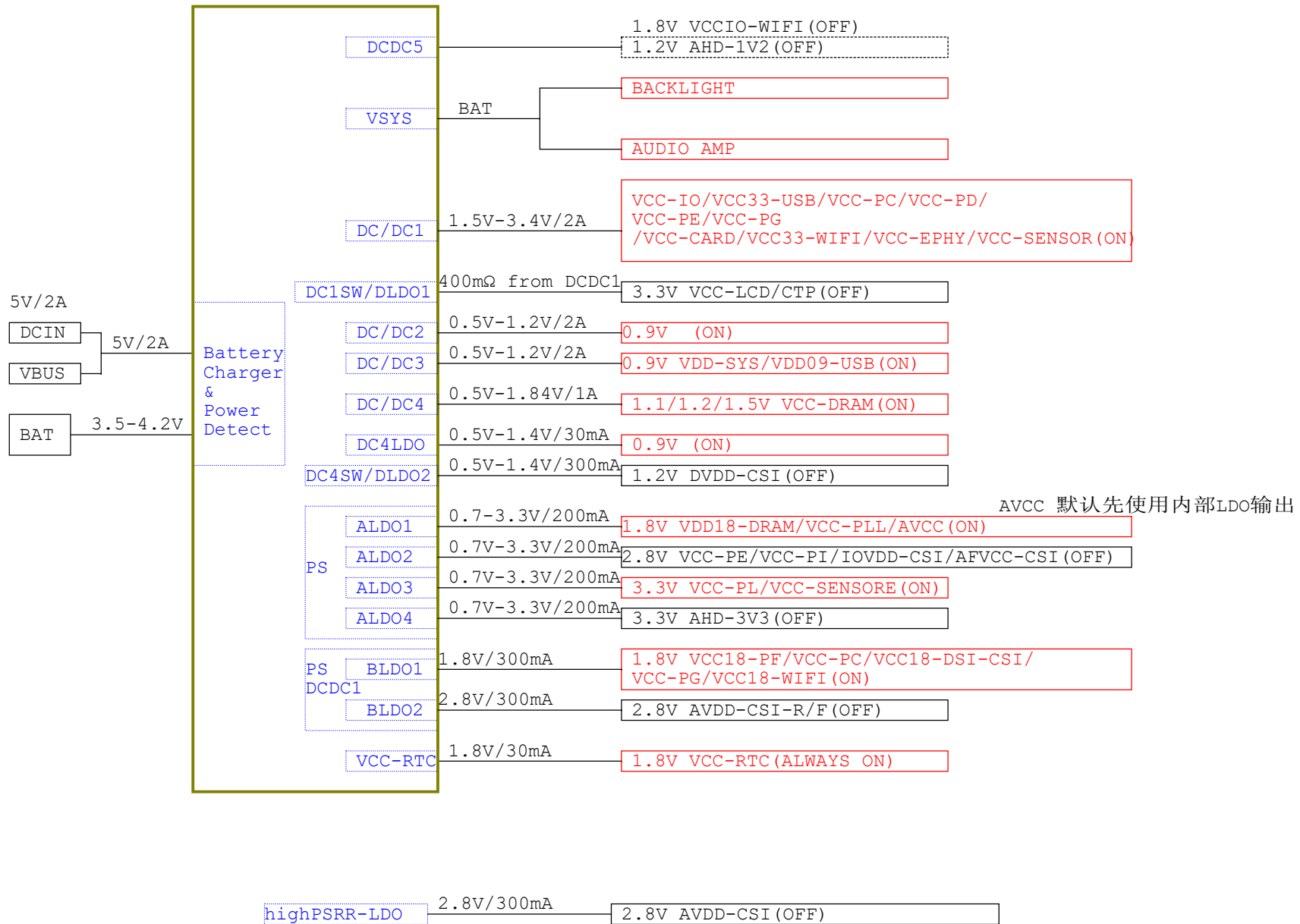


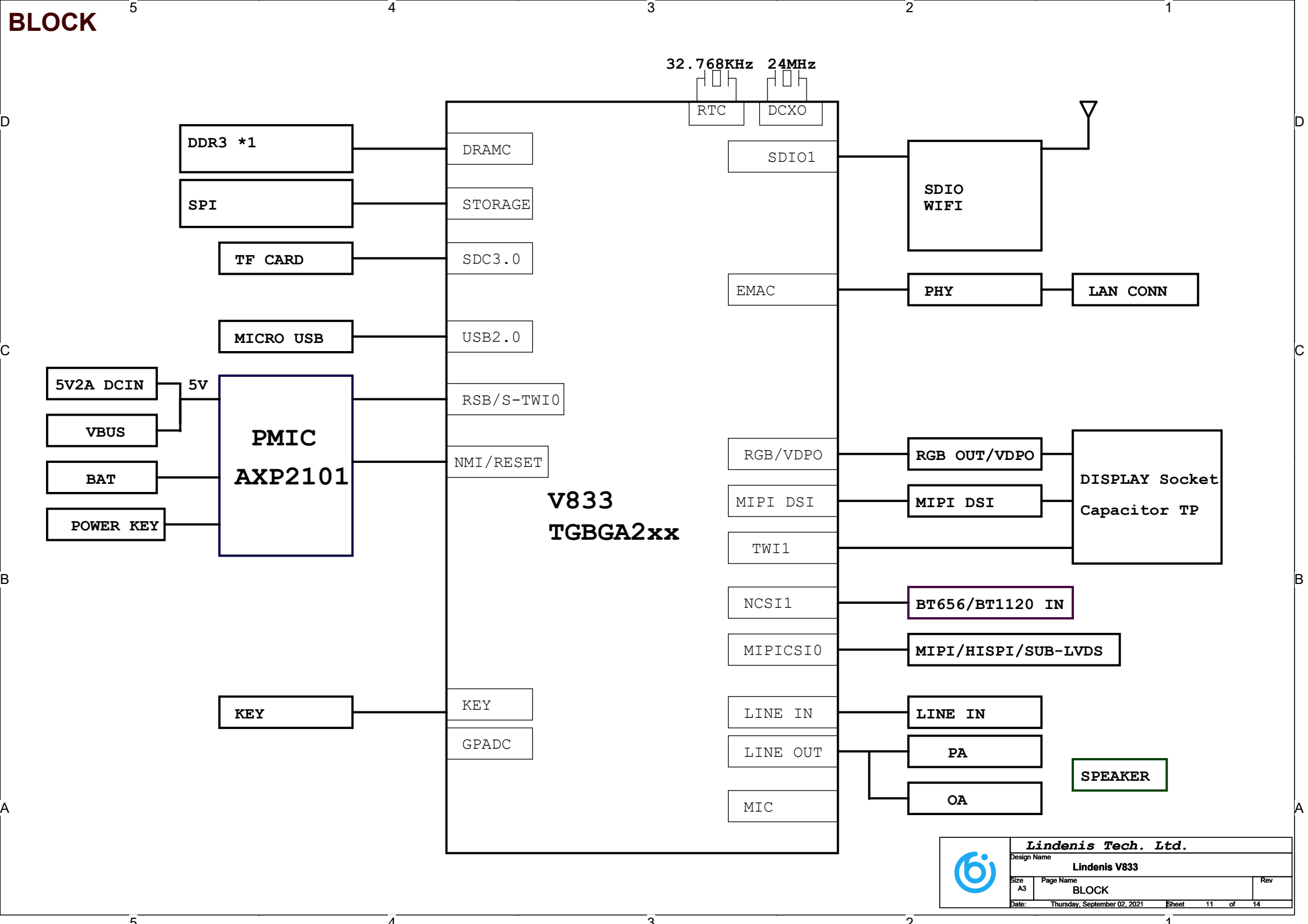
- 6 VDD-SYSFB << DCDC3-FB
- 7 PMU-SDA
- 7 PMU-SCK
- 6,14 AP-NMI
- 6 AP-RESET
- 11,14 PWRON-AXP2101 << PWRON
- 6,14 IRQ-AXP2101 << AP-NMI

# POWER TREE

## AXP2101

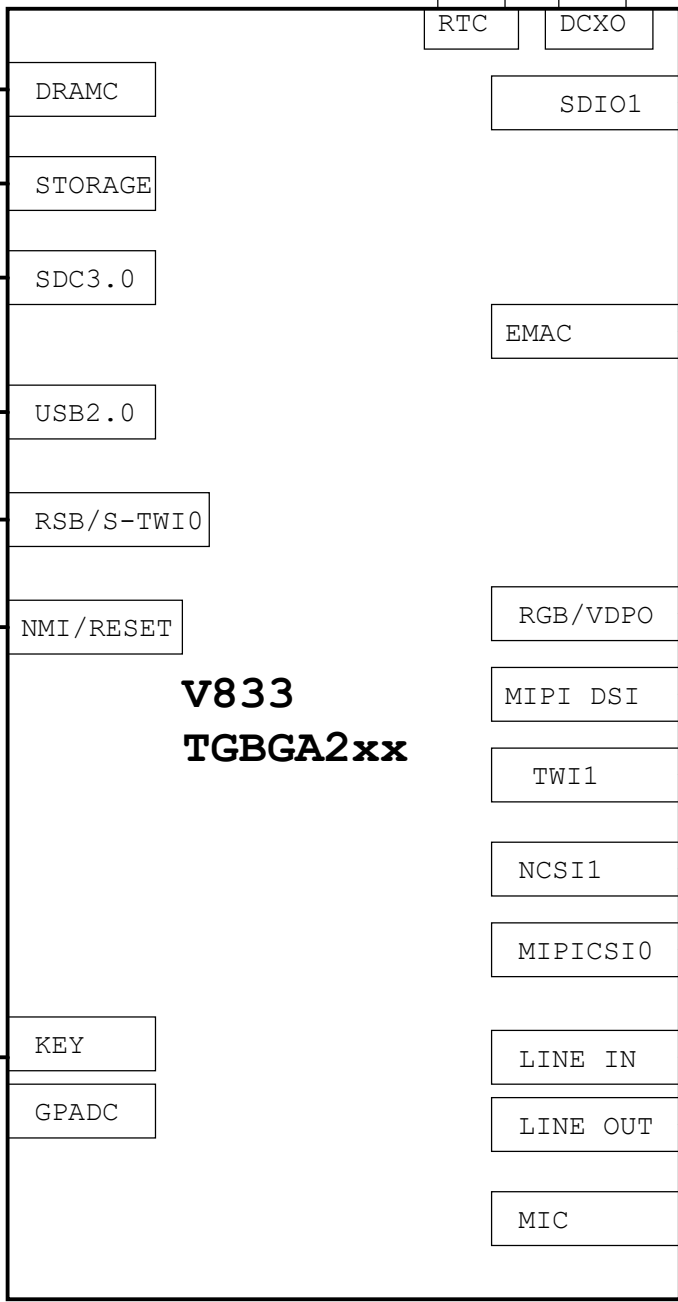
DEFAULT POWER ON  
 DEFAULT POWER OFF





**BLOCK**

32.768KHz 24MHz




# REVISION HISTORY

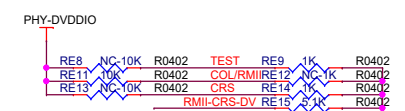
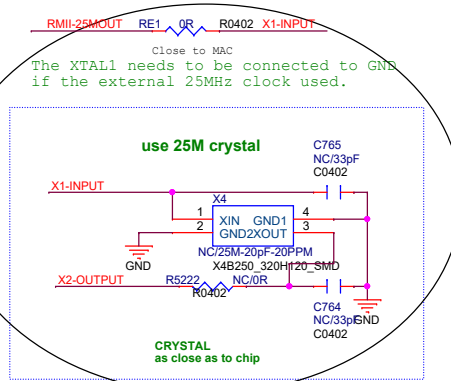
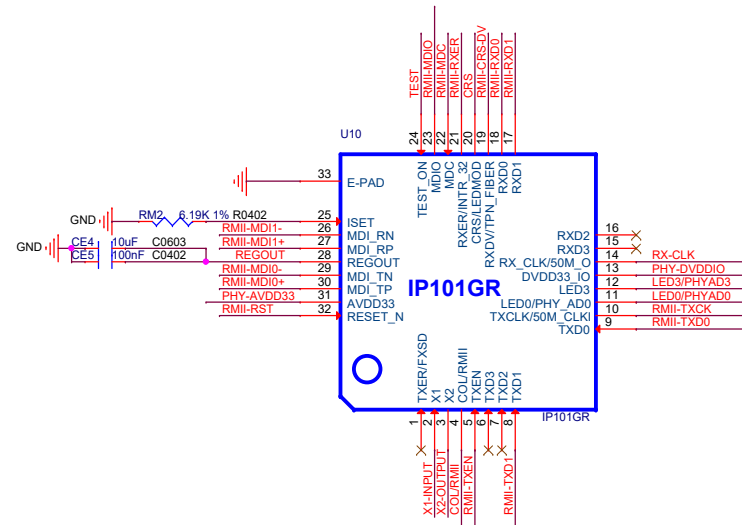
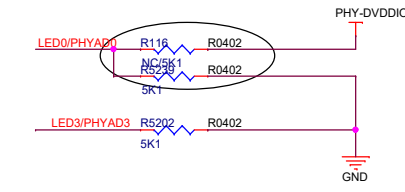
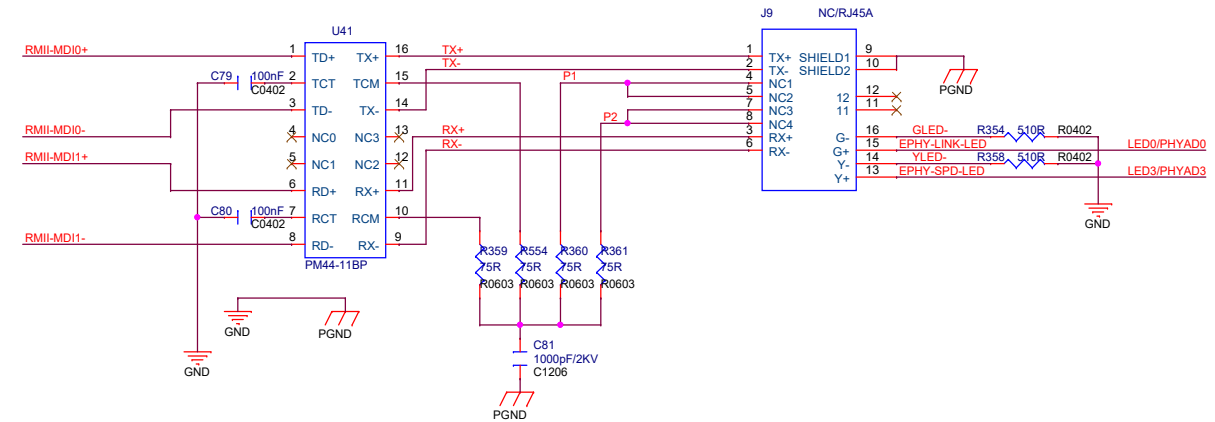
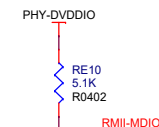
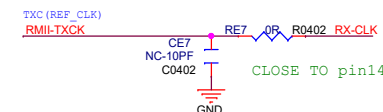
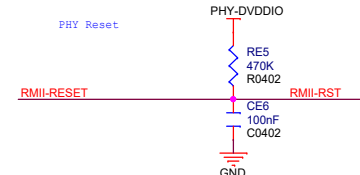
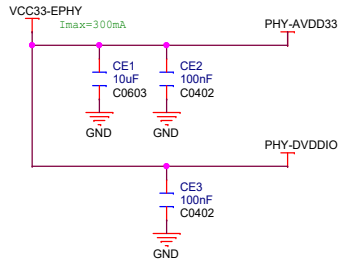
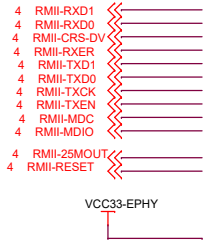
Revision	Description	Date	Drawn	Checked
Ver 0.1	Initial Version	2020-12-03	HXF	

## Schematics Index:

- P01: REVISION HISTORY
- P02: BLOCK
- P03: POWER TREE
- P04: GPIO ASSIGNMENT
- P05: POWER
- P06: CPU
- P07: PF/PG/PH/PL
- P08: DDR3 16X1
- P09: eMMC/NOR
- P10: CARD/USB/JTAG/UART
- P11: AUDIO/KEY/ADC
- P12: LCD/CTP/DSI/VDPO
- P13: MIPI CSI/CSI/PI
- P14: SENSOR/4G/ACC
- P15: WIFI

	<b>Linden Tech. Ltd.</b>		
	Design Name		
	<b>Linden V833</b>		
	Size A3	Page Name <b>REVISION HISTORY</b>	Rev
Date: <b>Tuesday, February 23, 2021</b> Sheet 11 of 14			

# RMII



FULL HIGH	Full down
TEST	TEST mode Normal mode
COL/RMII	SWII mode MII mode
CRS	LED model LED mode0
RX DV	- option

**Linden Tech. Ltd.**

Design Name: **Lindenis V833**

Size A3	Page Name RMII	Rev
Date: Tuesday, April 27, 2021		
Sheet		11 of 14