
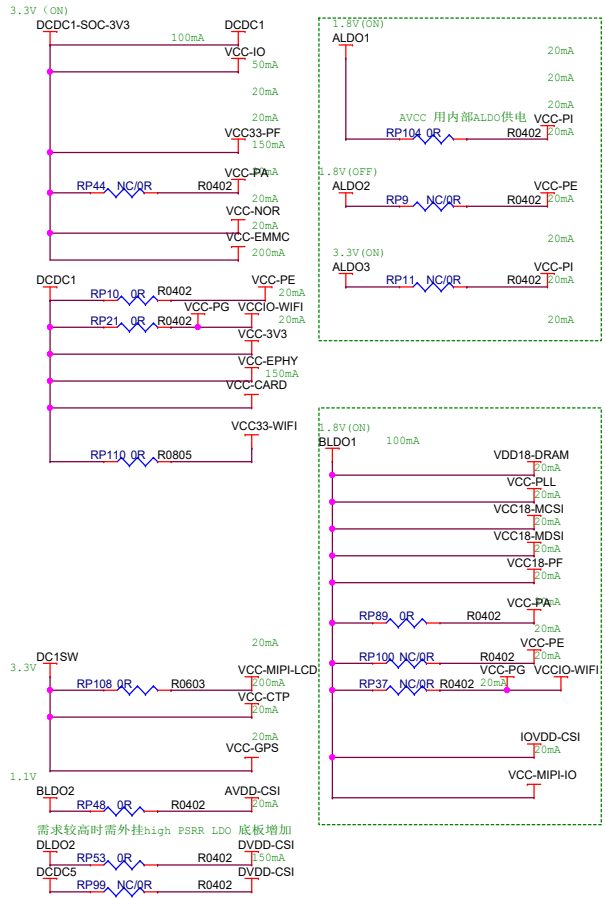


Revision	Description	Date	Drawn	Checked
Ver 0.1	Initial Version	2022-05-20		

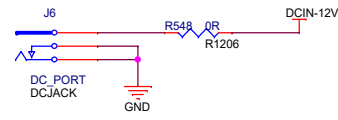
### Schematics Index:

- P01: REVISION HISTORY
- P02: POWER
- P03: CARD/USB/HUB
- P04: AUDIO
- P05: RGB/CTP/MIPI-DSI
- P06: LED/G-SENSOR
- P07: MIPI CSI
- P08: DVPCSI/RMII/RJ45
- P09: KEY/CON
- P10: WIFI
- P11: CORE BOARD

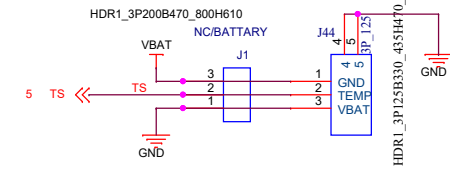
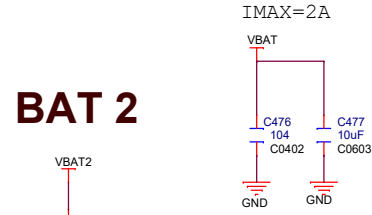
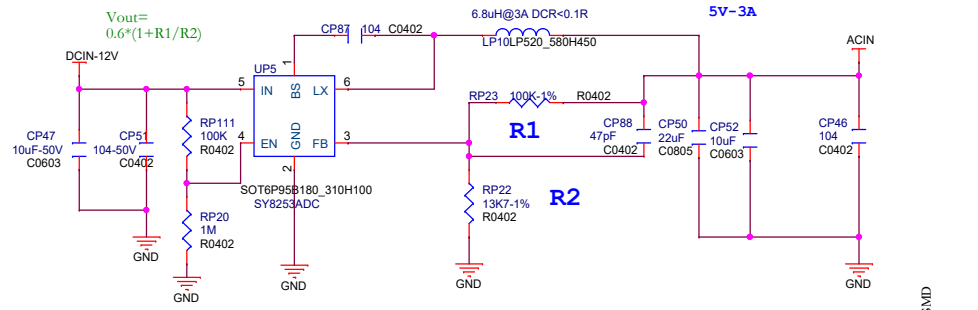
		<b>Linden Tech. Ltd.</b>	
		Design Name: <b>Linden V853</b>	
Size: A3	Page Name: <b>REVISION HISTORY</b>		Rev:
Date: Tuesday, December 06, 2022	Sheet: 11	of: 14	



# DC-12V

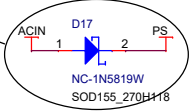


# DCIN12V->5V

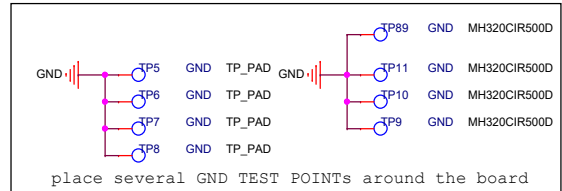
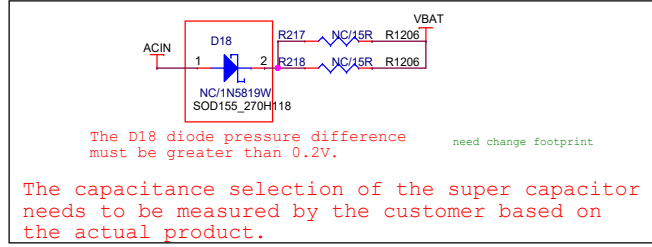


# BAT 2

If the load current is greater than 2A, a diode (D17) is added to relax the current limit of the PMU.



FOR Super capacitor solution (PMU REG64 bit[2:0]=000)



# AUDIO

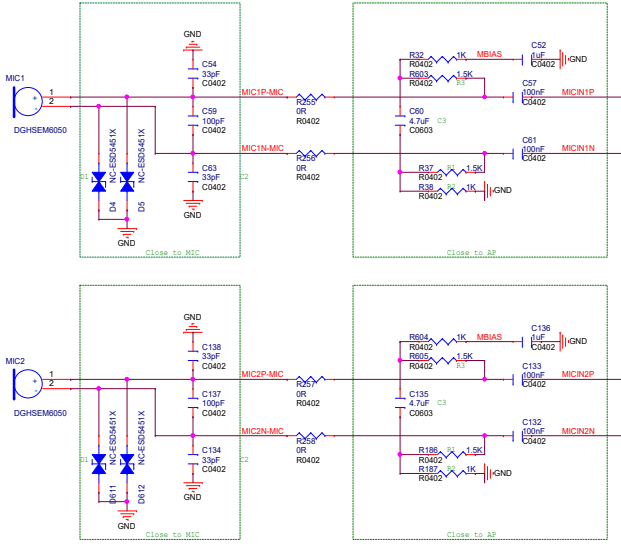
## MIC(Electret)

## SPEAKER

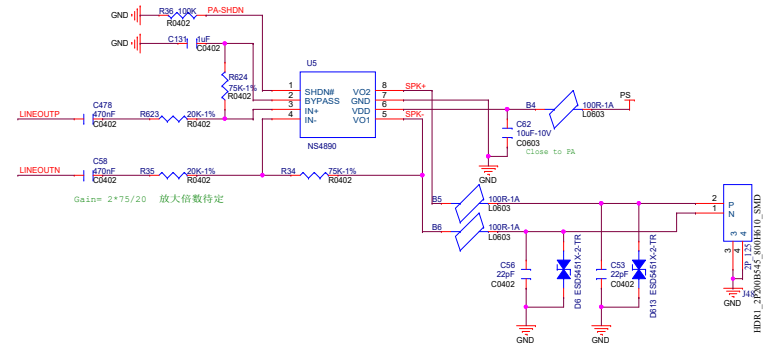
6 MBIAS  
6 MICIN1P  
6 MICIN1N  
6 MICIN2P  
6 MICIN2N

11 LINEOUTN  
11 LINEOUTP

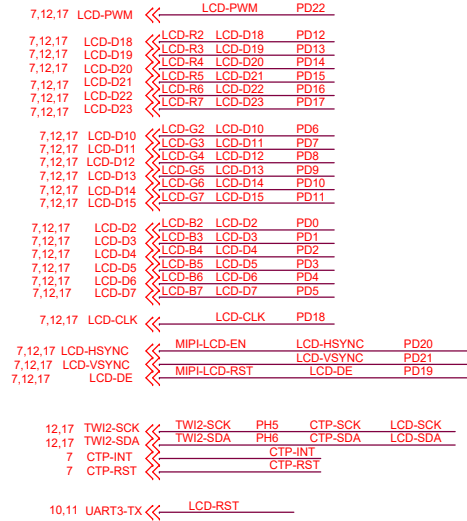
7 PA-SHDN



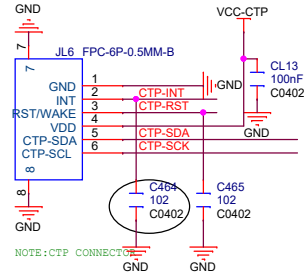
COMPONENT	Differential	Single-ended
1 1 3 1 C1 C3 D1	USE	NC
2	33pF	0Ω



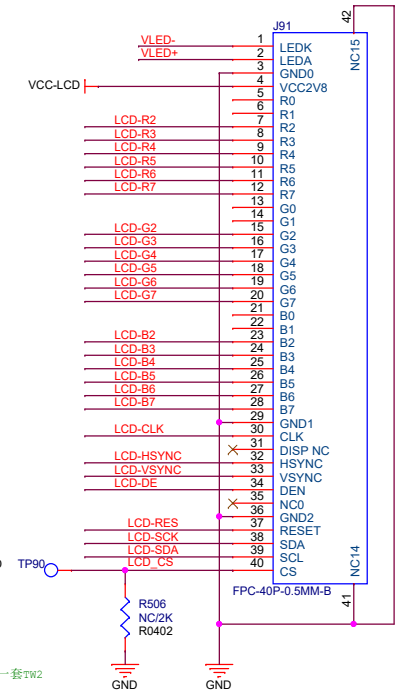
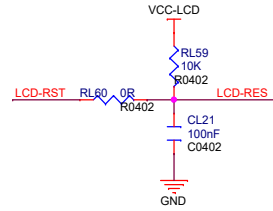
# LCD/CTP



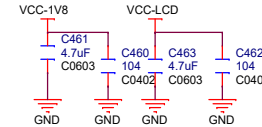
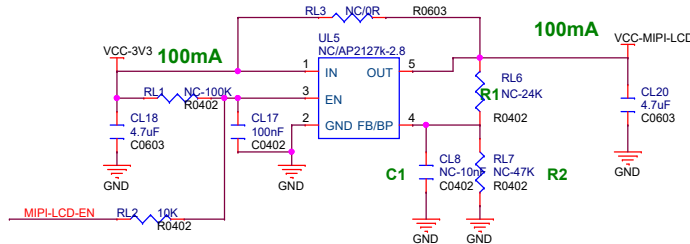
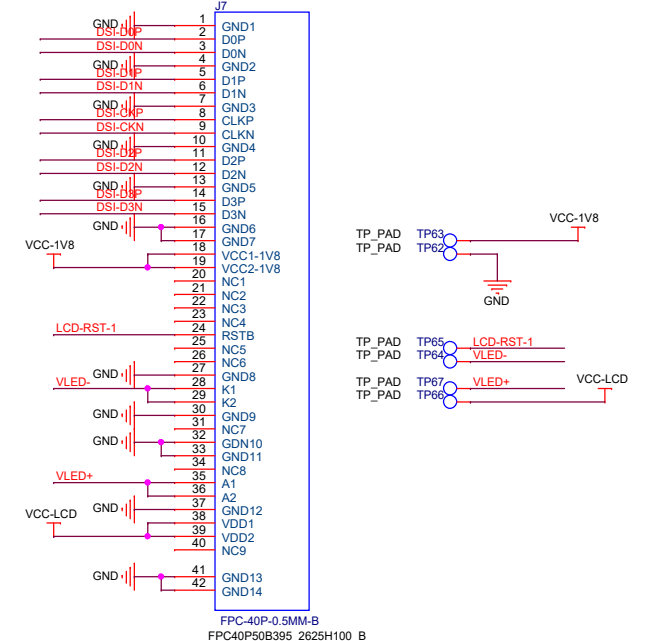
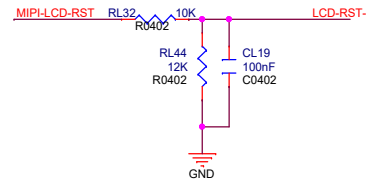
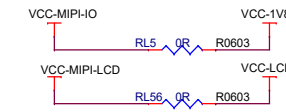
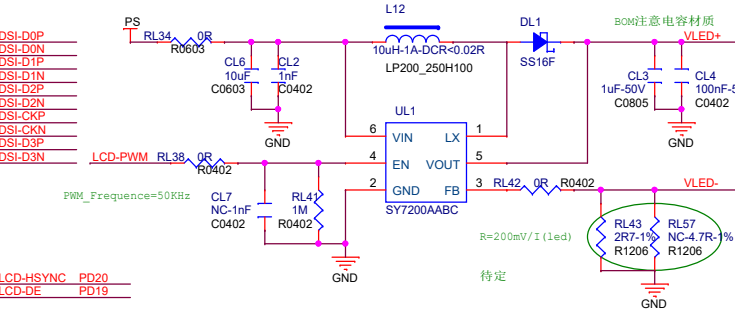
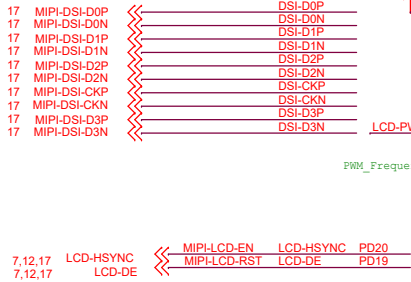
# CTP CN



# RGB LCD



# MIPI-DSI



**Linden Tech. Ltd.**

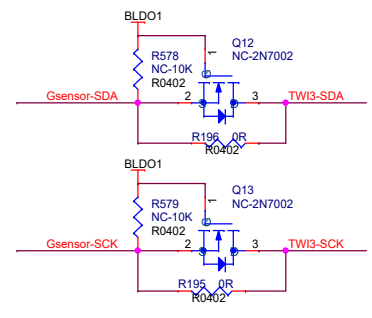
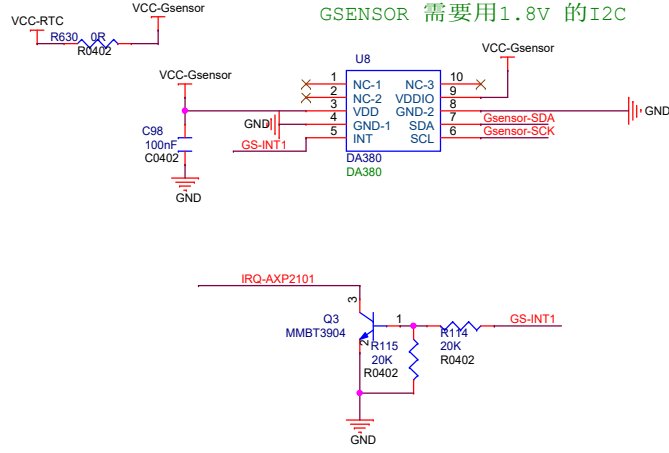
Design Name: **Linden V853**

Size: A3	Page Name: LCD/DSI/CTP	Rev:
Date: Tuesday, December 06, 2022		Sheet: 11 of 14

# G-SENSOR (DA380)

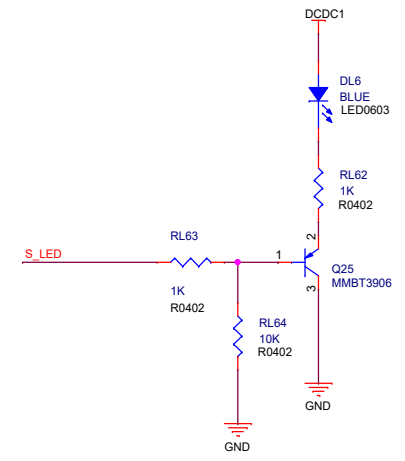
注： 停车监控开机电路  
GSENSOR 需要用1.8V 的I2C

- 12 TWI3-SCK <<-----
- 12 TWI3-SDA <<-----
- 5,15 AP-NMI <<----- IRQ-AXP2101

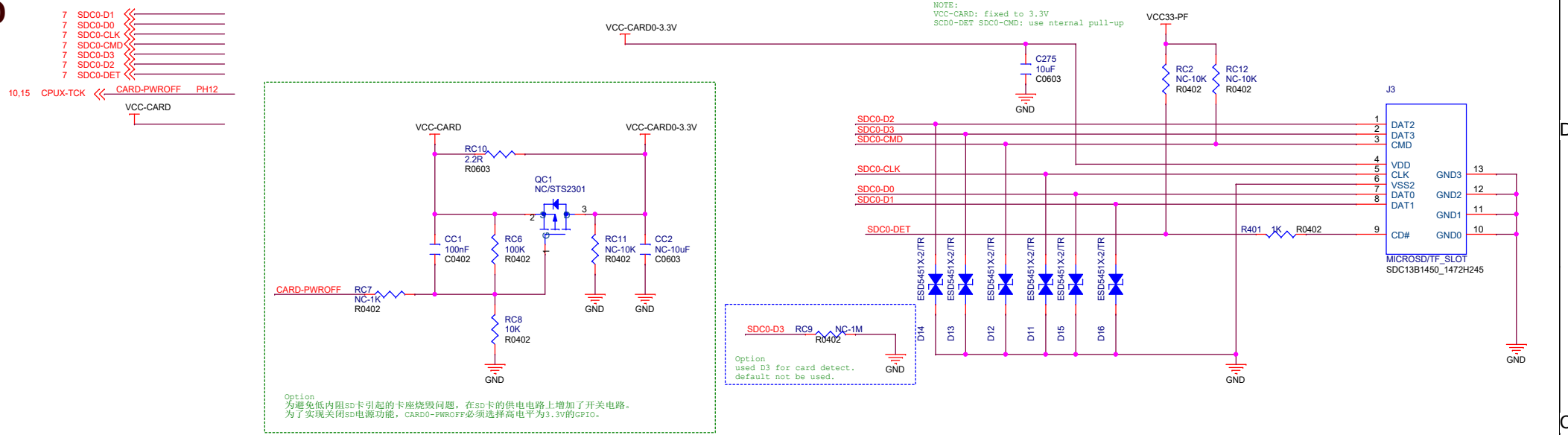


- 10 CPUX-TMS <<----- LED-REC PH11
- 10,15 S\_LED <<----- S\_LED

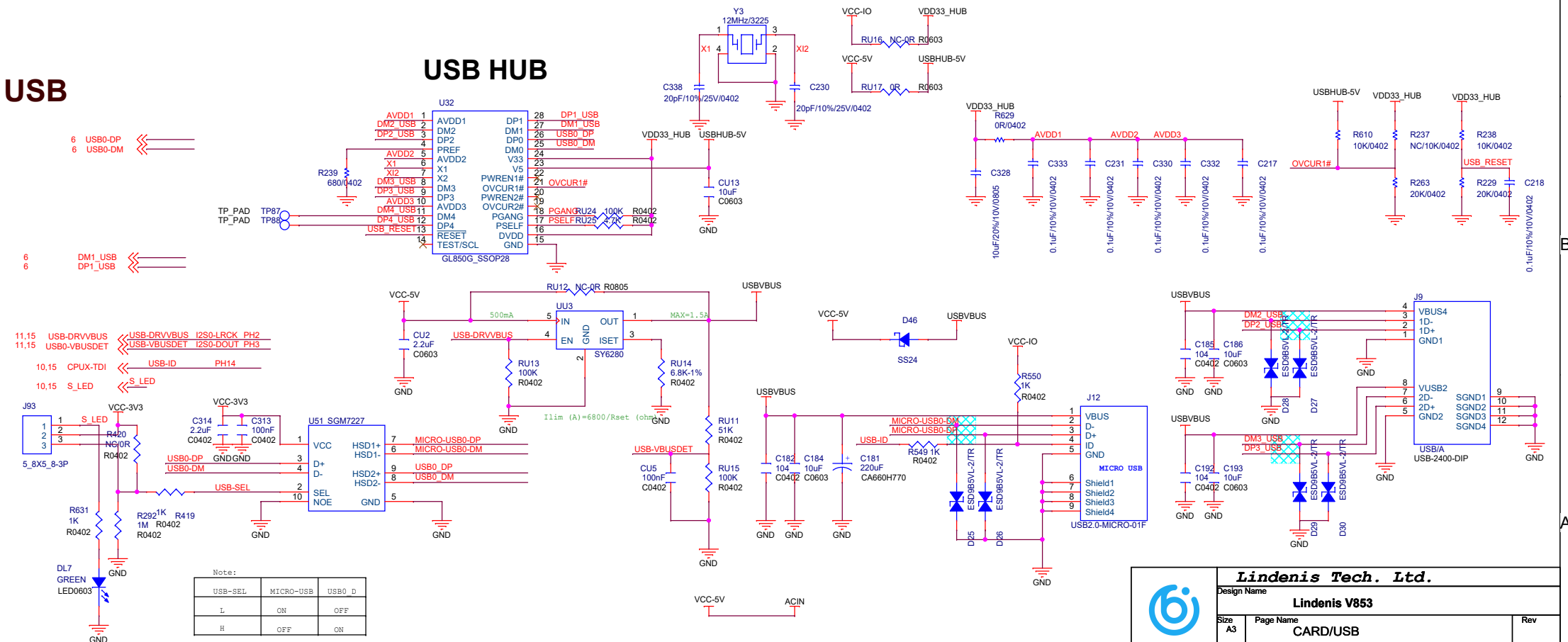
# LED



# CARD



# USB



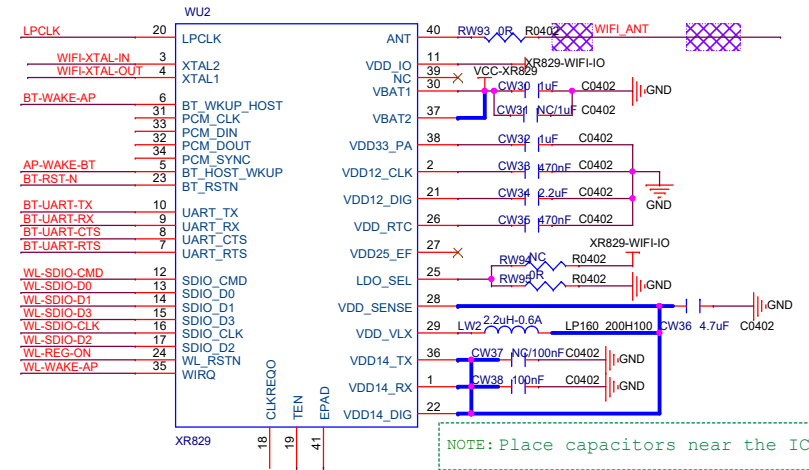
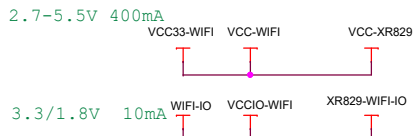
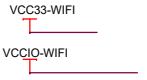
**Lindenis Tech. Ltd.**

Design Name  
**Lindenis V853**

Size: A3    Page Name: CARD/USB    Rev:   

Date: Tuesday, December 06, 2022    Sheet: 11 of 14

# WIFI BT



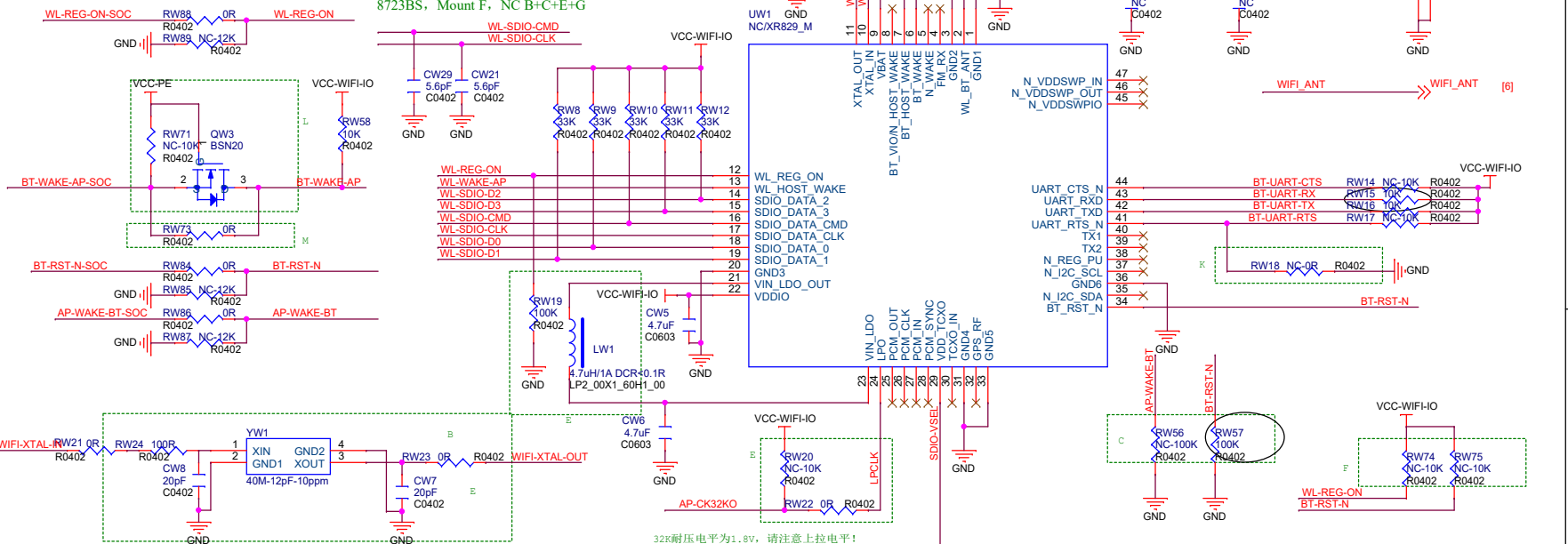
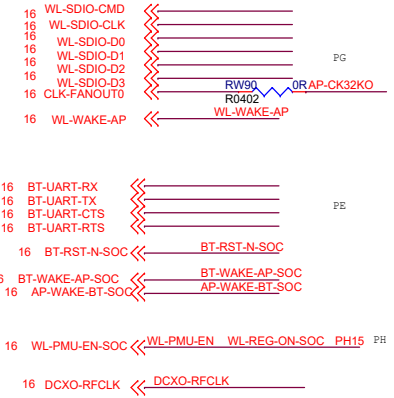
NOTE: Place capacitors near the IC

## APXXX/XR829

Note:  
AP6212/AP6234/AP6181/8723BS/AP6330, Y1=26M  
AP6335/AP6255, Y1=37.4M

Note:  
AP6212, Mount M+B+C+E, NC F+G  
AP6234, Mount M+B+C+E, NC F+G  
AP6181, Mount M+B+E, NC C+F+G  
AP6330, Mount M+B+C+E, NC F+G  
AP6335, Mount L+B+C+E+G, NC F  
AP6255, Mount L+B+C+E, NC F+G  
8723BS, Mount F, NC B+C+E+G

NOTE:  
注意SOC端GPIO和WiFi-IO电平匹配  
NOTE:3.3V 转1.8V 10K/12K电阻分压



Option: XR829与主控共高频时钟  
(未体现在参考PCB中)

		<b>Linden Tech. Ltd.</b>	
		Design Name <b>Lindenis V853</b>	
Size A3	Page Name WIFI	Rev	
Date:	Tuesday, December 06, 2022	Sheet	11 of 14

# CON

# GPIO

# KEY



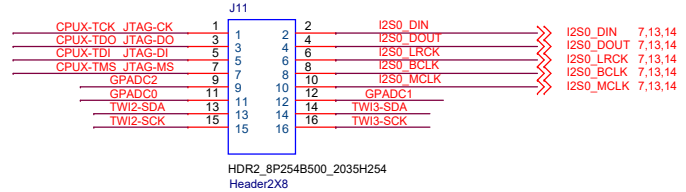
# POWER

# ADC

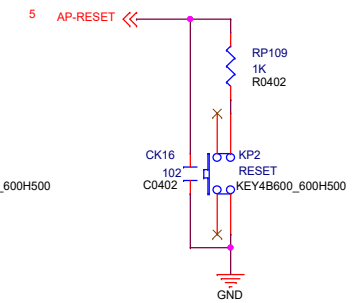
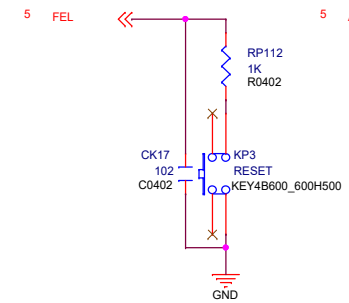
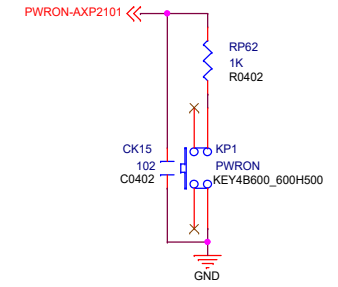
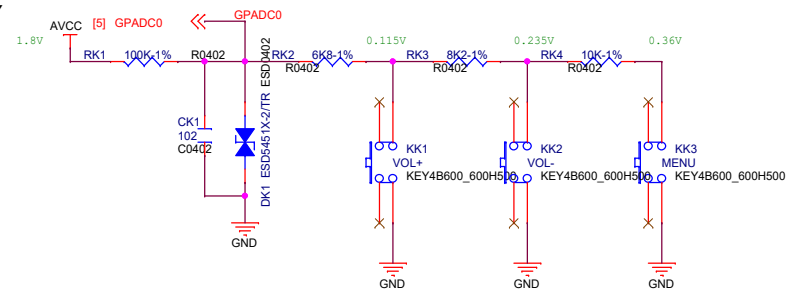
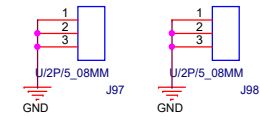
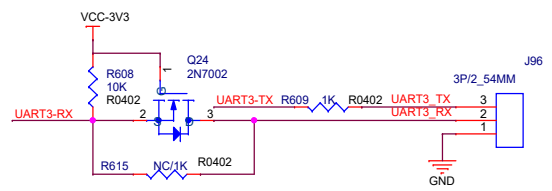
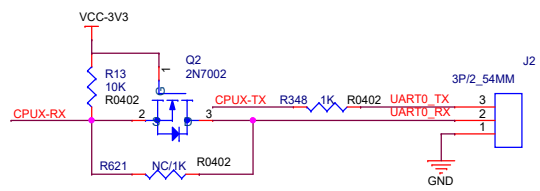
# UART

# E907-UART

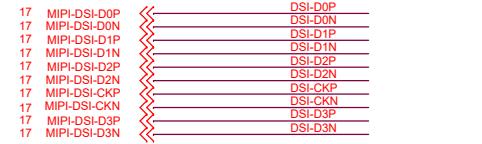
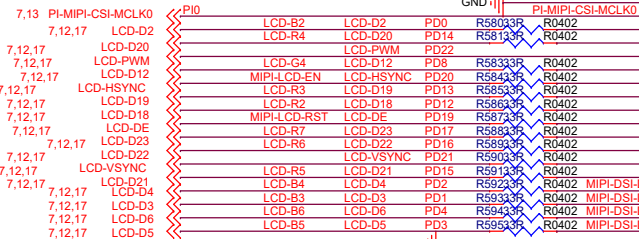
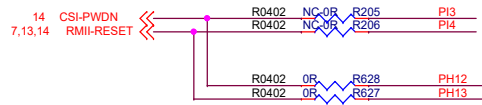
# I2S TWI UART



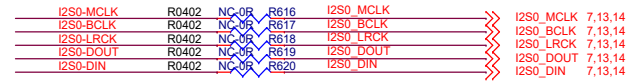
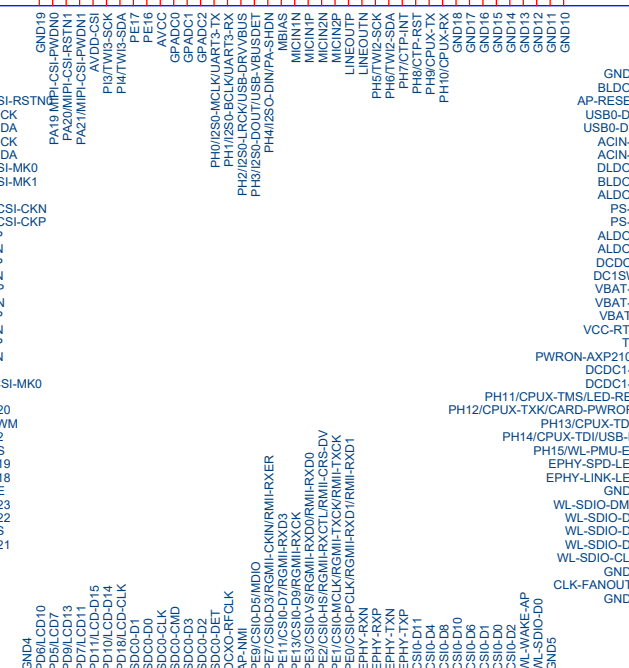
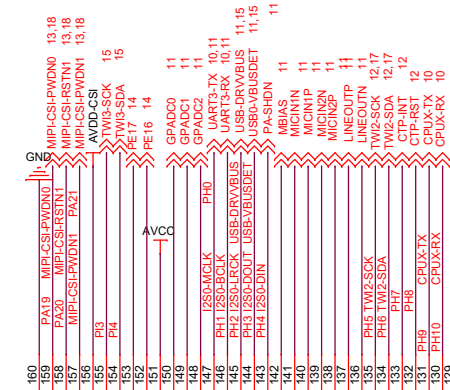
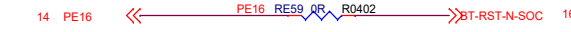
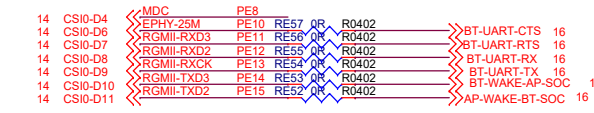
# DEBUG CPUX UART



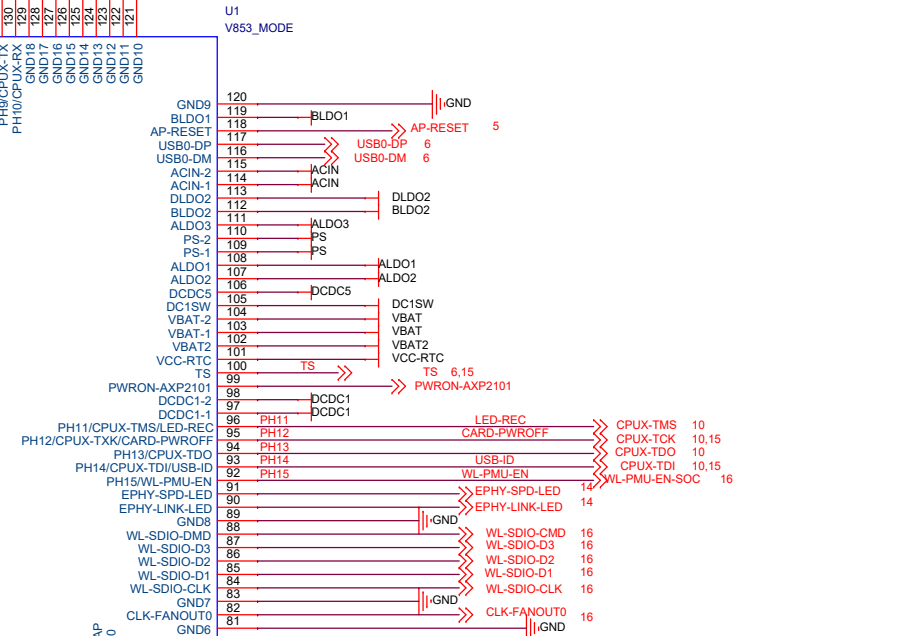




**PE OR MOUNT - BT, OR NC - DVP CSI**

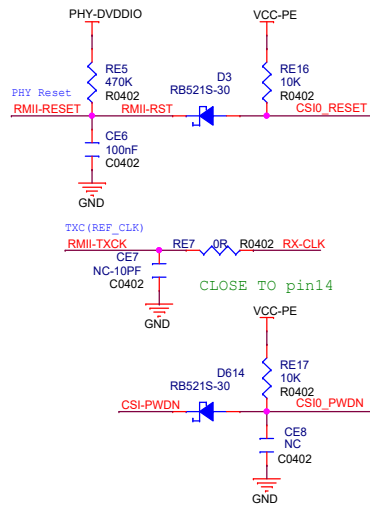
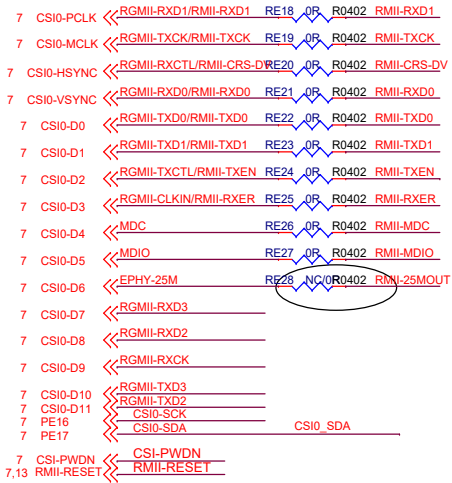


OR CLOSE TO V853 MODE

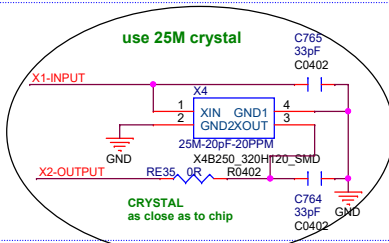


<b>Lindenis Tech. Ltd.</b>			
Design Name		<b>Lindenis V853</b>	
Size	Page Name	Rev	
A3	CORE BOARD		
Date:	Tuesday, December 06, 2022	Sheet	11 of 14

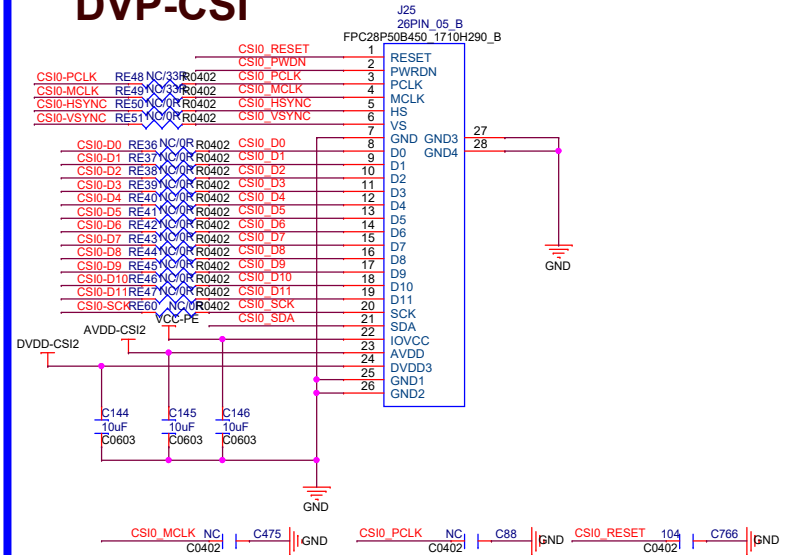
# PE CSI&RMII



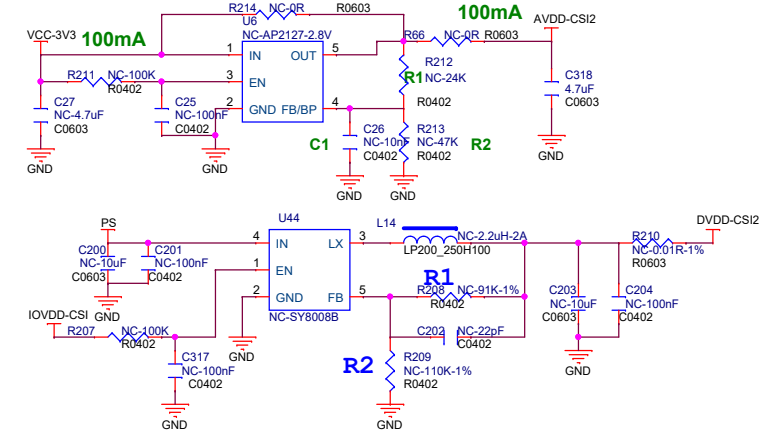
RMII-25MOUT RE1 NC/R R0402 X1-INPUT  
Close to MAC  
The XTAL1 needs to be connected to GND if the external 25MHz clock used.



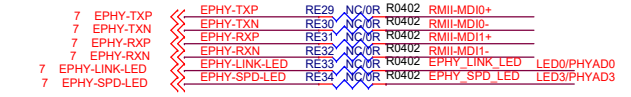
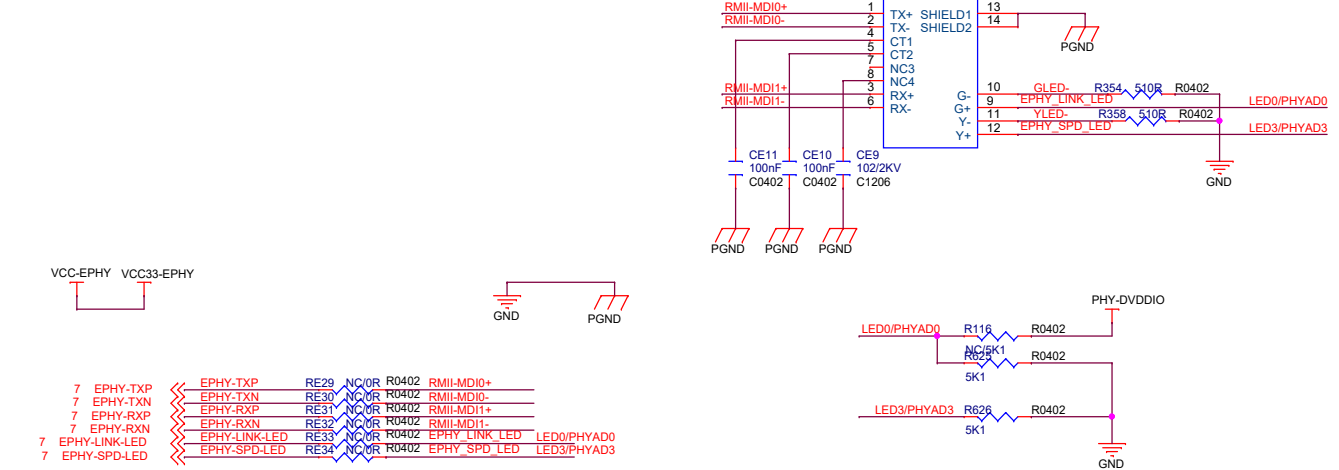
# DVP-CSI



# CSI2-POWER

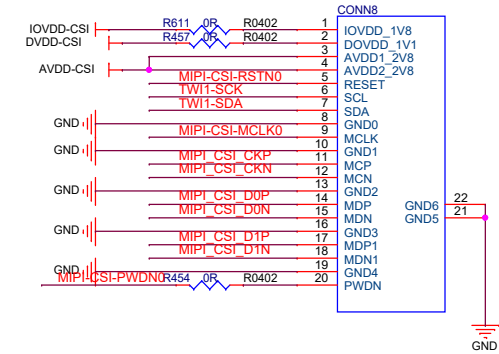


# JUST FOR TEST

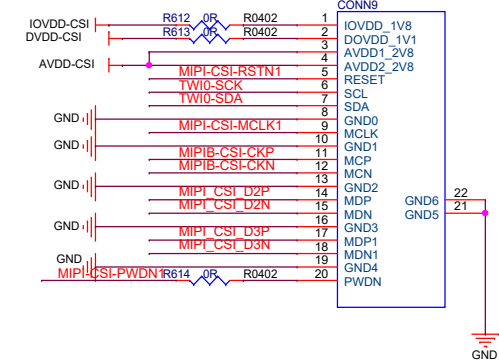


**Linden Tech. Ltd.**  
Design Name: **Lindenis V853**  
Size: A3 Page Name: **RGMII/CSI/EPHY** Rev: \_\_\_\_\_  
Date: Tuesday, December 06, 2022 Sheet: 11 of 14

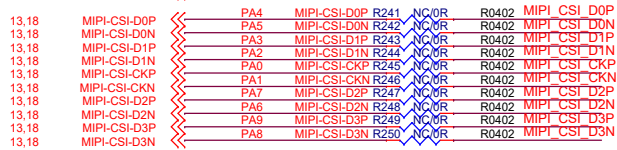
搭配PMU 方案时, 子板不支持MIPI-CSI 4lane+BT656



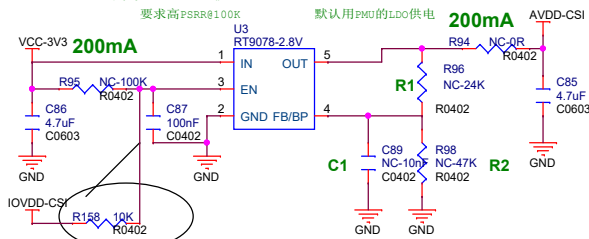
FPC22P50B450\_1540H290\_B  
Fpc20P0\_5\_B



FPC22P50B450\_1540H290\_B  
Fpc20P0\_5\_B



## 前置摄像头AVDD供电



IO	两组MIPI-2lane	MIPI-4lane
PA0	MIPIA-CSI-CKOP	MIPI-CSI-CKP
PA1	MIPIA-CSI-CKON	MIPI-CSI-CKN
PA2	MIPIA-CSI-D1N	MIPI-CSI-D1N
PA3	MIPIA-CSI-D1P	MIPI-CSI-D1P
PA4	MIPIA-CSI-D2N	MIPI-CSI-D2N
PA5	MIPIA-CSI-D2P	MIPI-CSI-D2P
PA6	MIPIB-CSI-D0N	MIPI-CSI-D0N
PA7	MIPIB-CSI-D0P	MIPI-CSI-D0P
PA8	MIPIB-CSI-D1N	MIPI-CSI-D1N
PA9	MIPIB-CSI-D1P	MIPI-CSI-D1P
PA10	MIPIB-CSI-D2N	MIPI-CSI-D2N
PA11	MIPIB-CSI-D2P	MIPI-CSI-D2P